## QUATECH, INC. MPI-600

Card Type Chipset/Controller I/O Options Maximum DRAM I/O controller Intel 80C188 Serial ports (2) N/A



CONNECTIONS			
Purpos 3	Location	Purpos 🔅	Location
Serial port 1	CN1	SCC channel "B" connector	J2
Serial port 2	CN2		

I/O ADDRESS SELECT								
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
3F8h	On	Off						
3E8h	On	Off	Off	Off	Off	Off	On	Off
2F8h	On	Off	On	Off	Off	Off	Off	Off
2E8h	On	Off	On	Off	Off	Off	On	Off
7F8h	Off							

Note: The address range for the MPI-600 can be set between 000h and 7F8h. The switches are a binary representation of the addresses. The switches have the following decimal values: switch 1=4, switch 2=2, switch 3=1, switch 4=8, switch 5=4, switch 6=2, switch 7=1, switch 8=8. Turn Off the switches and add the values of the Off switches to obtain the correct address.

	DMA CHANNEL SELECT	
DMA	J3	J4
1	Pins 1 & 3 closed	Pins 1 & 3 closed
3	Pins 2 & 4 closed	Pins 2 & 4 closed
None	All open	All open

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PARALLEL PORT CONFIGURATION		
J1	Designation	
Pins 1 - 4	Connected to ground	
Pin 5	Connected to 8256 pin P27	
Pin 6	Connected to 8256 Pin P26	
Pin 7	Connected to 8256 Pin P25	
Pin 8	Connected to 8256 Pin P24	

INTERRUPT SELECT			
IRQ	J5		
IRQ2	Pins 1 & 7 closed		
IRQ3	Pins 2 & 8 closed		
IRQ4	Pins 3 & 9 closed		
IRQ5	Pins 4 & 10 closed		
IRQ6	Pins 5 & 11 closed		
IRQ7	Pins 6 & 12 closed		

CURRENT LOOP POWER LEVEL SELECT			
Settinç	J6	J7	
+12 volts	Open	Closed	
-12 volts	Closed	Open	
External power source used	Open	Open	

SIGNAL CONFIGURATION					
Signal Type	Mode	J8	J9	J10	J11
RTxC/RRxC	V.35 (RS-422)	1 & 3 closed	1 & 3 closed	N/A	N/A
RTxC/RRxC	V.24 (RS-232)	2 & 4 closed	2 & 4 closed	N/A	N/A
RTxC/RRxC	Receive no clock	All open	All open	N/A	N/A
RxD	V.35 (RS-422)	N/A	N/A	1 & 4 closed	N/A
RxD	V.24 (RS-232)	N/A	N/A	2 & 5 closed	N/A
RxD	Current Loop	N/A	N/A	3 & 6 closed	N/A
TTxC	TRxCA	Open	N/A	N/A	1 & 3 closed
TTxC	RTxCA	N/A	Open	N/A	2 & 4 closed
TTxC	Send no clock	N/A	N/A	N/A	All open

INTERFACE CONFIGURATION			
Setting J12			
	Pins 1 & 16, 2 & 17, 5 & 20, 6 & 21, 7 & 22, 9 & 24, 10		
RS-232	& 25, 11 & 26, 13 & 28, 14 & 29, 15 & 30 closed		
Note: If transmitting a clock, close pins 4 & 19. If receiving a clock, close pins 8 & 23 for RxC mode and close			
pins 12 & 27 for TxC mode.			

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INTERFACE CONFIGURATION				
Setting	J12			
RS-422	Pins 1 & 16, 18 & 33, 5 & 20, 6 & 21, 26 & 41, 29 & 44,			
	30 & 45 closed			
Note: If transmitting a clock, close Pins 17 & 32, 19 & 34. If receiving a clock, close Pins 22 & 37, 23 & 38 for				
RxC mode and close Pins 24 & 39, 27 & 42 for TxC mode.				
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INTERFACE CONFIGURATION			
Setting	J12		
Current Loop	Pins 1 & 16, 2 & 17, 3 & 18, 7 & 22, 9 & 24, 11 & 40,		
	14 & 43, 14 & 29, 15 & 30 closed		
Note: If using positive voltage, close Pins 21 & 36. If using negative voltage, close Pins 20 & 35.			