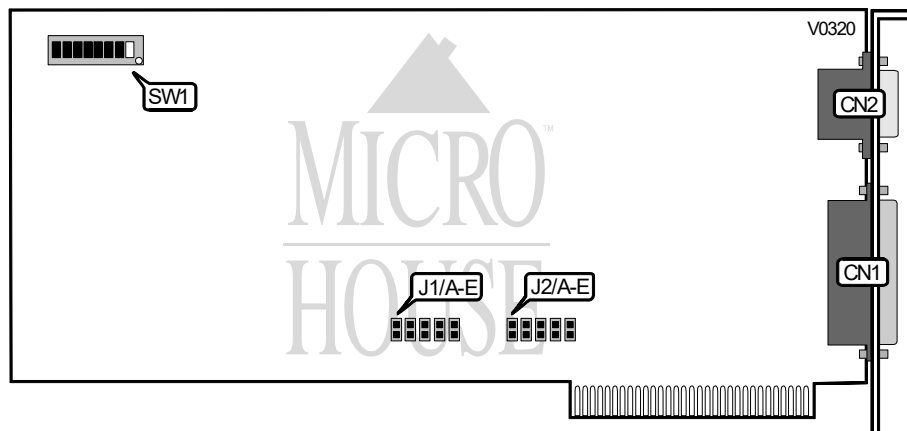


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ISI502Q

Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Serial controller
 16450/16550A
 Serial ports (2)
 N/A



CONNECTIONS			
Purpose :	Location	Purpose :	Location
Serial port 1	CN1	Serial port 2	CN2

PRIMARY PORT I/O ADDRESS SELECT								
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
2F8h	Off	On	On	On	On	Off	On	Off
3F8h	Off	On	On	On	On	On	On	Off
3E8h	Off	Off	On	On	On	On	On	Off
2E8h	Off	Off	On	On	On	Off	On	Off

SECONDARY PORT I/O ADDRESS SELECT								
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
2F0h	Off	On	On	On	On	Off	On	Off
3F0h	Off	On	On	On	On	On	On	Off
3E0h	Off	Off	On	On	On	On	On	Off
2E0h	Off	Off	On	On	On	Off	On	Off

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PRIMARY SERIAL PORT INTERRUPT SELECT					
IRQ	J2/A	J2/B	J2/C	J2/D	J2/E
IRQ4	Open	Open	Closed	Open	Open
IRQ3	Open	Closed	Open	Open	Open
IRQ2	Closed	Open	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Closed

SECONDARY SERIAL PORT INTERRUPT SELECT					
IRQ	J1/A	J1/B	J1/C	J1/D	J1/E
IRQ5	Open	Open	Open	Closed	Open
IRQ3	Open	Closed	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open
IRQ2	Closed	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Closed