EVEREX SYSTEMS, INC.
MAGIC I/O EV-170/170A

Card Type   Multi I/O
Chipset/Controller   Krueger Company
I/O Options   Parallel port, serial ports (2)
Maximum DRAM   N/A
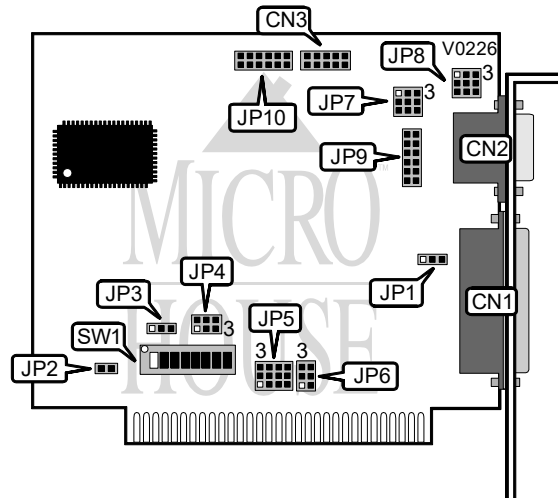
CONNECTIONS			
Purpose	Location	Purpose	Location
Parallel port	CN1	Serial port 2	CN3
Serial port 1	CN2		

SERIAL PORT CONFIGURATION			
ort 1 (CN2)	SW1/1	SW1/2	SW1/3	SW1/4
í COM1	On	Off	Off	Off
COM2	Off	On	Off	Off
COM3	Off	Off	On	Off
COM4	Off	Off	Off	On
Disabled	Off	Off	Off	Off
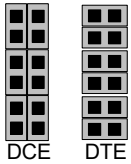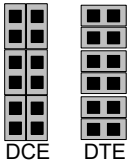
SERIAL PORT CONFIGURATION			
ort 2 (CN3)	SW1/5	SW1/6	SW1/7	SW1/8
í COM1	On	Off	Off	Off
COM2	Off	On	Off	Off
COM3	Off	Off	On	Off
COM4	Off	Off	Off	On
Disabled	Off	Off	Off	Off

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SERIAL PORT INTERRUPT SELECTION			
Port 1 (CN2)	Port 2 (CN3)	P5	P5
í IRQ4	í IRQ3	Pins 4 & 5 closed	Pins 2 & 3 closed
IRQ3	IRQ4	Pins 1 & 2 closed	Pins 5 & 6 closed
IRQ2	IRQ2	Pins 10 & 11 closed	Pins 11 & 12 closed
IRQ5	IRQ5	Pins 7 & 8 closed	Pins 8 & 9 closed

SERIAL PORT NORMAL/FORCED TRUE CONFIGURATION			
Port 1	Port 2	JP7	JP8
í Normal	í Normal	Pins 1 & 2, 4 & 5, 7 & 8 closed	Pins 1 & 2, 4 & 5, 7 & 8 closed
Forced True	Forced True	Pins 2 & 3, 5 & 6, 8 & 9 closed	Pins 2 & 3, 5 & 6, 8 & 9 closed

SERIAL PORT'S DTE/DCE CONFIGURATION	
Primary serial port CN2	Secondary serial port CN3
Jumper JP9	Jumper JP10
DCE DTE	DCE DTE

PARALLEL PORT CONFIGURATION	
LPT	JP6
í 2	Pins 3 & 6 closed
1	Pins 1 & 4 closed
3	Pins 2 & 5 closed
Disabled	Open

PARALLEL PORT INTERRUPT SELECTION	
IRQ	JP1
í 7	Pins 2 & 3 closed
5	Pins 1 & 2 closed

PARALLEL PORT MODE SELECTION	
Mode	JP3
í Unidirectional (printer)	Pins 1 & 2 closed
Bidirectional	Pins 2 & 3 closed

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CLOCK CONFIGURATION	
Setting	JP2
í Enabled	Closed
Disabled	Open

CLOCK INTERRUPT SELECTION	
IRQ	JP4
í Disabled	Open
5	Pins 2 & 5 closed
7	Pins 3 & 6 closed
2	Pins 1 & 4 closed