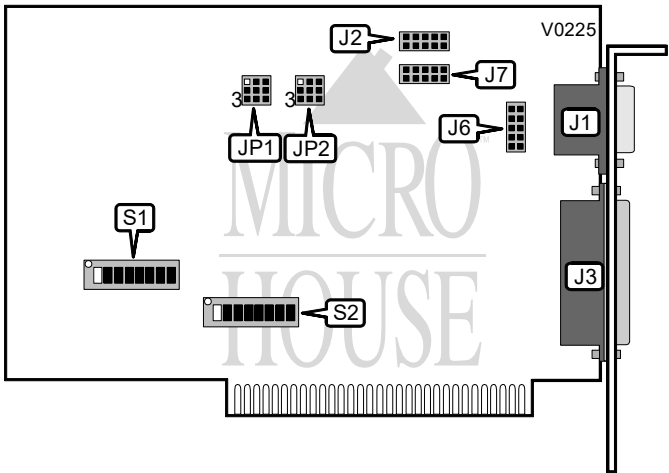


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PBIO-220

Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Multi I/O
Unknown
Parallel port, serial ports (2)
N/A



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	J1	Parallel port	J3
Serial port 2	J2		

SERIAL PORT CONFIGURATION			
Port 1 (J1)	Setting	1/3	1/6
COM1	Enabled	On	Off
COM3	Disabled	Off	On





















SERIAL PORT CONFIGURATION			
Port 2 (J2)	Setting	1/4	1/7
COM2	Enabled	On	Off
COM4	Disabled	Off	On

Continued on next page . . .

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SERIAL PORT INTERRUPT SELECTION					
Port 1 (J1)	Port 2 (J2)	S2/1	S2/2	S2/3	S2/4
í IRQ4	í IRQ3	On	Off	On	Off
IRQ3	IRQ4	Off	On	Off	On

SERIAL PORT NORMAL/FORCED TRUE CONFIGURATION			
Port 1	Port 2	J4	J5
í Normal	í Normal	Pins 2 & 3, 5 & 6, 8 & 9 closed	Pins 2 & 3, 5 & 6, 8 & 9 closed
Forced True	Forced True	Pins 1 & 2, 4 & 5, 7 & 8 closed	Pins 1 & 2, 4 & 5, 7 & 8 closed

SERIAL PORT'S DTE/DCE CONFIGURATION			
Primary serial port J1		Secondary serial port J2	
Jumper J6		Jumper J7	
1  TX	1  TX	1  TX	1  TX
 RD	 RD	 RD	 RD
 RTS	 RTS	 RTS	 RTS
 CTS	 CTS	 CTS	 CTS
9  DTR	9  DTR	9  DTR	9  DTR
DTE	DCE	DTE	DCE

PARALLEL PORT CONFIGURATION			
Parallel port	LPT	1/1	1/5
í Enabled	1	On	Off
Enabled	2	Off	On

PARALLEL PORT INTERRUPT SELECTION		
IRC	S2/5	S2/6
í 7	On	Off
5	Off	On
Disabled	Off	Off

FACTORY CONFIGURED - DO NOT ALTER	
Switch	Position
S1/2	N/A
S1/8	N/A
S2/7	N/A
S2/8	N/A