IMAGRAPH CORPORATION
TI-1210-4 (REVISION C & LATER)

Category	Video
Video Types Supported	XVGA
Video Processor	TMS34010
Highest Resolution Supported	1280 x 1024
Data Bus Type	16-bit ISA
Memory Type	DRAM/VRAM
Maximum Onboard Memory	512KB DRAM, 1536KB VRAM
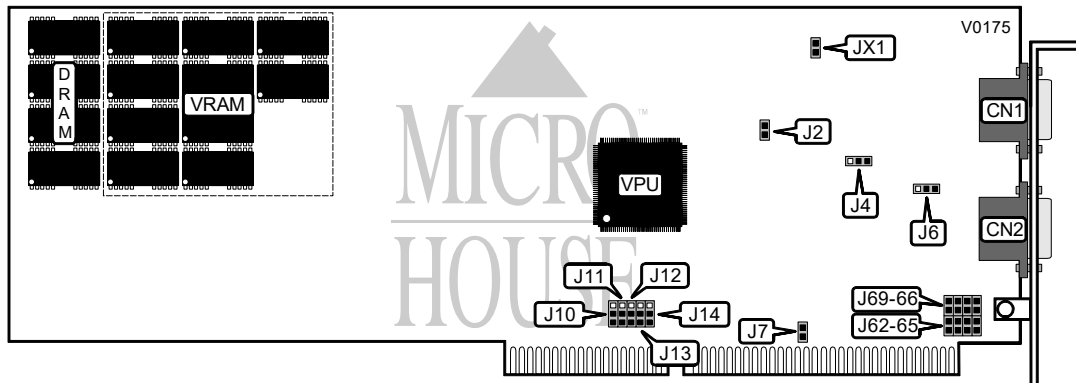
CONNECTIONS			
Purpose	Location	Purpose	Location
9-pin video analog port	CN1	VGA Pass-Through connector	CN2

FACTORY CONFIGURED - DO NOT ALTER	
Jumper	Position
J2	Open

BASE ADDRESS SELECTION								
Address	J62	J63	J64	J65	J66	J67	J68	J69
C700h	Open	Open	Closed	Closed	Open	Open	Open	Closed
CC00h	Open	Open	Closed	Closed	Closed	Closed	Open	Open
CF00h	Open	Open	Closed	Closed	Open	Open	Open	Open
D700h	Open	Open	Closed	Open	Open	Open	Open	Closed
DF00h	Open	Open	Closed	Open	Open	Open	Open	Open

EMULATION CONFIGURATION	
Setting	J7
Enabled	Open
Disabled	Closed

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VGA PASS-THROUGH CONFIGURATION	
Setting	JX1
í Disabled	Open
Enabled	Closed
Note: Emulation jumper (J7) must be disabled when VGA Pass-Through is enabled.	

FOURTH CABLE OUTPUT SYNC SELECTION			
Outpu from VGA	Output fro n TI-1210-4	J4	J6
í Composite sync signal	í Composite sync signal	Pins 1 & 2 closed	Pins 1 & 2 closed
Separate horizontal sync	Separate horizontal sync	Pins 2 & 3 closed	Pins 2 & 3 closed

HOST BUS INTERRUPT SELECTION					
IRQ	J10	J11	J12	J13	J14
í Disabled	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Pins 1 & 2
4	Open	Open	Open	Pins 1 & 2	Open
5	Open	Open	Pins 1 & 2	Open	Open
6	Open	Pins 1 & 2	Open	Open	Open
7	Pins 1 & 2	Open	Open	Open	Open
10	Open	Open	Open	Open	Pins 2 & 3
11	Open	Open	Open	Pins 2 & 3	Open
12	Open	Open	Pins 2 & 3	Open	Open
14	Open	Pins 2 & 3	Open	Open	Open
15	Pins 2 & 3	Open	Open	Open	Open
Note: Pins designated should be in the closed position.					