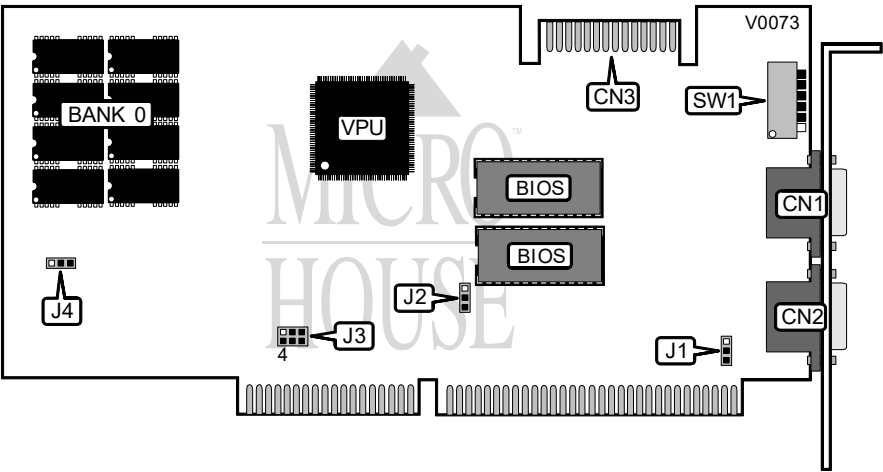


PEAKTRON COMPUTER, INC.  
PVGA 2/4/8-DRAM (VERSION 1)

Category	Video
Video Types Supported	CGA/EGA/Monochrome/VGA/XVGA
Video Processor	TVGA 8900
Highest Resolution Supported	1024 x768
Data Bus Type	16-bit ISA
Memory Type	DRAM
Maximum Onboard Memory	1024KB



CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	CN1	VGA feature connector	CN3
9-pin TTL video port	CN2		

FACTORY CONFIGURED SETTINGS	
Switch	Position
SW1/4	On

MONITOR TYPE SELECTION		
Type	SW1/1	SW1/2
VGA mode	On	On
EGA mode	Off	On
CGA mode	On	Off
MDA/Hercules mode	Off	Off

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PVGA 2/4/8-DRAM (VERSION 1)

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SCAN RATE SELECTION	
Rate	SW1/3
f > 48KHz	On
< 48 - 49KHz	Off

ADDRESS DECODE SELECTION	
Setting	SW1/5
f Fast	Off
Slow	On

VIDEO INTERRUPT SELECTION	
IRQ 2/9	J1
f Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

BUS DATA PATH SELECTION	
BUS	SW1/6
f 16-bit	Off
8-bit	On

BUS SIZE DETECTION	
Setting	J2
f Autodetect	Pins 2 & 3 closed
Standard interface	Pins 1 & 2 closed

DRAM CLOCK SELECTION	
Setting	J4
f 40mhz (2 DRAM)	Pins 2 & 3 closed
48mhz (4/8 DRAM)	Pins 1 & 2 closed

DRAM CONFIGURATION			
ize	B nk 0	B nk 1	J3
256KB	(2) 44256	NONE	Pins 1 & 2, 4 & 5 closed
512KB	(4) 44256	NONE	Pins 1 & 4, 2 & 5 closed
1024KB	(4) 44256	(4) 44256	Pins 2 & 3, 5 & 6 closed
Note: The exact locations of banks 0 and 1 are unidentified.			