# MICRODYNE CORPORATION
# E X O S  2 0 5 T

| | |
|---|---|
| **NIC Type** | Ethernet |
| **Processor** | Intel 80186 |
| **Processor Speed** | 8MHz |
| **Chip Set** | Intel 82586 |
| **Maximum Onboard Memory** | 512KB DRAM |
| **Data Bus** | 8/16-bit ISA |
| **Network Transfer Rate** | 10Mbps |
| **Topology** | Linear Bus |
| **Wiring Type** | RG-58A/U 50ohm coaxial |
| | AUI transceiver via DB-15 port |
| **Boot ROM** | Available (location unidentified) |



Note: Exact location is unidentified.
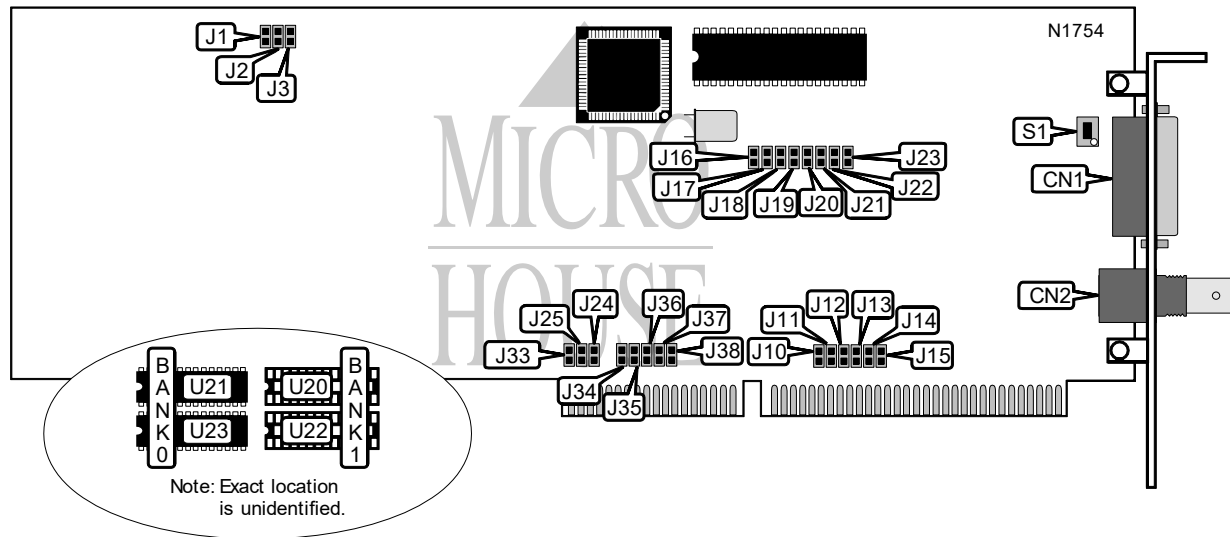
| CONNECTIONS | | | |
|---|---|---|---|
| **Function** | **Label** | **Function** | **Label** |
| AUI transceiver via DB-15 port | CN1 | RG-58A/U 50ohm coaxial connector | CN2 |

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| **Setting** | **Label** | **Position** |
| í  SQE heartbeat enabled | J2 | Open |
| SQE heartbeat disabled | J2 | Closed |
| í  Carrier detect enabled | J3 | Open |
| Carrier detect disabled | J3 | Closed |
| í  Auto-detect bus size | J33 | Closed |
| Force 8-bit bus size | J33 | Open |

| CABLE TYPE | |
|---|---|
| **Setting** | **S1** |
| RG-58A/U 50ohm coaxial | On |
| AUI transceiver via DB-15 port | Off |

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| BASE I/O ADDRESS | | |
|---|---|---|
| **Setting** | **J24** | **J25** |
| 300h | Closed | Closed |
| í 310h | Open | Closed |
| 320h | Closed | Open |
| 330h | Open | Open |

| BOOT ROM ADDRESS | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| **Setting** | **J16** | **J17** | **J18** | **J19** | **J20** | **J21** | **J22** | **J23** |
| C0000h | Closed | Open | Open | Open | Open | Open | Open | Open |
| C4000h | Open | Closed | Open | Open | Open | Open | Open | Open |
| C8000h | Open | Open | Closed | Open | Open | Open | Open | Open |
| CC000h | Open | Open | Open | Closed | Open | Open | Open | Open |
| D0000h | Open | Open | Open | Open | Closed | Open | Open | Open |
| D4000h | Open | Open | Open | Open | Open | Closed | Open | Open |
| D8000h | Open | Open | Open | Open | Open | Open | Closed | Open |
| DC000h | Open | Open | Open | Open | Open | Open | Open | Closed |
| í Disabled | Open | Open | Open | Open | Open | Open | Open | Open |

| INTERRUPT | | | | | | |
|---|---|---|---|---|---|---|
| **Setting** | **J10** | **J11** | **J12** | **J13** | **J14** | **J15** |
| í IRQ2/9 | Closed | Open | Open | Open | Open | Open |
| IRQ3 | Open | Closed | Open | Open | Open | Open |
| IRQ4 | Open | Open | Closed | Open | Open | Open |
| IRQ5 | Open | Open | Open | Closed | Open | Open |
| IRQ6 | Open | Open | Open | Open | Closed | Open |
| IRQ7 | Open | Open | Open | Open | Open | Closed |
| IRQ10 | Open | Open | Open | Open | Open | Open |
| IRQ11 | Open | Open | Open | Open | Open | Open |
| IRQ12 | Open | Open | Open | Open | Open | Open |
| IRQ13 | Open | Open | Open | Open | Open | Open |
| IRQ14 | Open | Open | Open | Open | Open | Open |

| INTERRUPT (CON'T) | | | | | |
|---|---|---|---|---|---|
| **Setting** | **J34** | **J35** | **J36** | **J37** | **J38** |
| í IRQ2/9 | Open | Open | Open | Open | Open |
| IRQ3 | Open | Open | Open | Open | Open |
| IRQ4 | Open | Open | Open | Open | Open |
| IRQ5 | Open | Open | Open | Open | Open |
| IRQ6 | Open | Open | Open | Open | Open |
| IRQ7 | Open | Open | Open | Open | Open |
| IRQ10 | Open | Open | Open | Open | Closed |
| IRQ11 | Open | Open | Open | Closed | Open |
| IRQ12 | Open | Open | Closed | Open | Open |
| IRQ13 | Open | Closed | Open | Open | Open |
| IRQ14 | Closed | Open | Open | Open | Open |

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| DRAM CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 256KB | (2) 256K x 4 | None |
| 512KB | (2) 256K x 4 | (2) 256K x 4 |

| DRAM JUMPER | |
|---|---|
| **Size** | **J1** |
| 256KB | Closed |
| 512KB | Open |