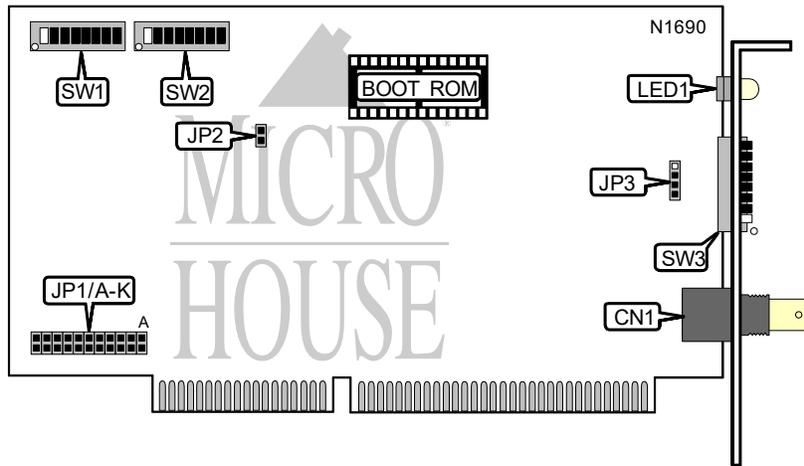


LAN PROFESSIONAL 16-BIT ARCLINK

NIC Type	ARCnet
Network Transfer Rate	2.5Mbps
Data Bus	16-bit ISA
Topology	Star
Wiring Type	RG-62A/U 93ohm coaxial
Boot ROM	Available



CONNECTIONS	
Function	Label
RG-62A/U 93ohm coaxial port	CN1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
16-Bit Arclink set in enhanced mode	JP2	Closed
16-Bit Arclink set in compatible mode	JP2	Open

BASE MEMORY ADDRESS SELECTION (ENHANCED MODE)					
Address	SW1/1	SW1/2	SW2/1	SW2/2	SW2/3
D000h	On	On	On	On	On
CC00h	Off	Off	On	On	On

BASE MEMORY ADDRESS SELECTION (ENHANCED MODE)					
Address	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
D000h	On	Off	Off	On	Off
CC00h	On	Off	Off	On	On

BASE MEMORY ADDRESS SELECTION (COMPATIBLE MODE)					
Address	SW1/1	SW1/2	SW2/1	SW2/2	SW2/3
D000h	On	On	On	On	On
CC00h	On	On	On	On	On

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BASE MEMORY ADDRESS SELECTION (COMPATIBLE MODE)					
Address	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
D000h	On	Off	Off	On	Off
CC00h	On	Off	Off	On	On

BASE I/O ADDRESS SELECTION (ENHANCED MODE)						
Address	SW1/3	SW1/4	SW2/5	SW2/6	SW2/7	SW2/8
í 2E0h	Off	On	Off	Off	Off	On
2F0h	Off	On	Off	Off	Off	Off
300h	Off	Off	On	On	On	On

INTERRUPT SELECTION						
IRQ	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F
í 2	Closed	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Closed	Open	Open
6	Open	Open	Closed	Open	Open	Open
7	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Open	Open	Open
12	Open	Open	Open	Open	Open	Open
14	Open	Open	Open	Open	Open	Open
15	Open	Open	Open	Open	Open	Open

INTERRUPT SELECTION (CON'T)					
IRQ	JP1/G	JP1/H	JP1/I	JP1/J	JP1/K
í 2	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Open
4	Open	Open	Open	Open	Open
5	Open	Open	Open	Open	Open
6	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Open
10	Closed	Open	Open	Open	Open
11	Open	Closed	Open	Open	Open
12	Open	Open	Closed	Open	Open
14	Open	Open	Open	Open	Closed
15	Open	Open	Open	Closed	Open

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NODE ADDRESS SELECTION								
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1	Off	On						
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
5	Off	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off						
255	Off							

Note: A total of 255 node address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/8 is the Most Significant Bit and switch SW1/1 is the Least Significant Bit. The switches have the following decimal values: SW1/8=128, SW1/7=64, SW1/6=32, SW1/5=16, SW1/4=8, SW1/3=4, SW1/2=2, SW1/1=1. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)

RESPONSE AND RECONFIGURATION TIMEOUTS SETTING		
Response Time	Reconfiguration Time	JP3
74.7 μ Sec	840 mSec	Open
383.4 μ Sec	1680 mSec	Pins 3 & 4 closed
561.8 μ Sec	1680 mSec	Pins 1 & 2 closed
1118.6 μ Sec	1680 mSec	Pins 1 & 2, 3 & 4 closed

DIAGNOSTIC LED(S)
Note: The purpose of LED1 is unidentified.