Chapter 5: Jumper Settings

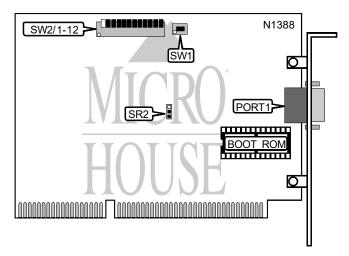
OLÍVETTI

NCU 9172 TYPE 1 (G0552)

Token Ring 4/16Mbps 16-bit ISA NIC Type Transfer Rate Data Bus Topology Wiring Type

Ring Shielded twisted pair

Boot ROM Available



CONNECTIONS	
Purpose	Location
Shielded twisted pair port	PORT1

USER CONFIGURABLE		
Purpose	Location	Setting
í Factory configured - do not alter	SW2/1	N/A

BASE I/O ADDRESS SELECTION		
Address	SW2/9	SW2/10
A20h, A30h	Off	Off
A24h, A40h	On	Off
A50h, A60h	Off	On
A54h, A70h	On	On

INTERRUPT SELECTION		
IRQ	SW2/7	SW2/8
í 2/9	On	On
3	On	Off
10	Off	On
11	Off	Off

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DMA CHANNEL SELECTION			
Channel SW2/11 SW2/12			
í 5	On	On	
6	On	Off	
7	Off	On	
Programmed I/O	Off	Off	

		BIOS ADDRES	SS SELECTION		
Address	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6
C0000h	On	On	On	On	On
C2000h	On	On	On	On	Off
C4000h	On	On	On	Off	On
C6000h	On	On	On	Off	Off
C8000h	On	On	Off	On	On
CA000h	On	On	Off	On	Off
í CC000h	On	On	Off	Off	On
CE000h	On	On	Off	Off	Off
D0000h	On	Off	On	On	On
D2000h	On	Off	On	On	Off
D4000h	On	Off	On	Off	On
D6000h	On	Off	On	Off	Off
D8000h	On	Off	Off	On	On
DA000h	On	Off	Off	On	Off
DC000h	On	Off	Off	Off	On
DE000h	On	Off	Off	Off	Off
E0000h	Off	On	On	On	On
E2000h	Off	On	On	On	Off
E4000h	Off	On	On	Off	On
E6000h	Off	On	On	Off	Off
E8000h	Off	On	Off	On	On
EA000h	Off	On	Off	On	Off
EC000h	Off	On	Off	Off	On
EE000h	Off	On	Off	Off	Off
F0000h	Off	Off	On	On	On
F2000h	Off	Off	On	On	Off
F4000h	Off	Off	On	Off	On
F6000h	Off	Off	On	Off	Off
F8000h	Off	Off	Off	On	On
FA000h	Off	Off	Off	On	Off
FC000h	Off	Off	Off	Off	On
FE000h	Off	Off	Off	Off	Off

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Chapter 5: Jumper Settings OLIVETTI NCU 9172 TYPE 1 (G0552)

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BOOT ROM CONFIGURATION	
Setting	SW1
í Disabled	On
Enabled	Off

	COMPATIBILITY (CONFIGURATION
	Setting	SR2
í Standard		Pins 2 & 3 closed
	Chips & Technologies	Pins 1 & 2 closed