**Chapter 5: Jumper Settings**
ACER, INC.
5 2 1 0 - 0 0 4

| | |
|---|---|
| **NIC Type** | Arcnet |
| **Transfer Rate** | 2.5Mbps |
| **Data Bus** | 8-bit ISA |
| **Topology** | Linear bus |
| **Wiring Type** | RG-62A/U 93ohm coaxial |
| **Boot ROM** | Available |



| NODE ADDRESS SELECTION | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| Address | SW2/1 | SW2/2 | SW2/3 | SW2/4 | SW2/5 | SW2/6 | SW2/7 | SW2/8 |
| 1 | Off | On | On | On | On | On | On | On |
| 2 | On | Off | On | On | On | On | On | On |
| 3 | Off | Off | On | On | On | On | On | On |
| 4 | On | On | Off | On | On | On | On | On |
| 251 | Off | Off | On | Off | Off | Off | Off | Off |
| 252 | On | On | Off | Off | Off | Off | Off | Off |
| 253 | Off | On | Off | Off | Off | Off | Off | Off |
| 254 | On | Off | Off | Off | Off | Off | Off | Off |
| 255 | Off | Off | Off | Off | Off | Off | Off | Off |
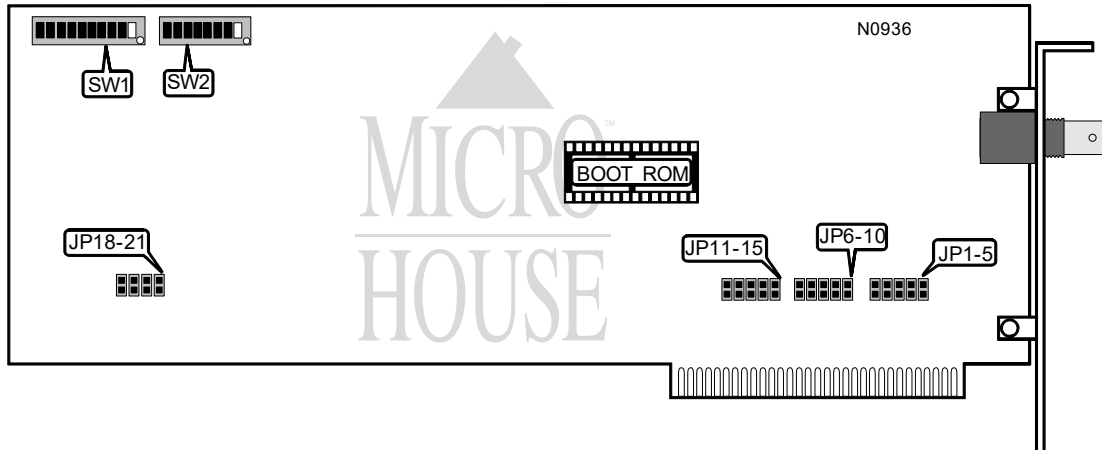
Note: A total of 255 node address settings are available.  The switches are a binary representation of the decimal node addresses.  Switch SW2/1 is the Least Significant Bit and switch SW2/8 is the Most Significant Bit. The switches have the following decimal values: switch SW2/1=1, SW2/2=2, SW2/3=4, SW2/4=8, SW2/5=16, SW2/6=32, SW2/7=64, SW2/8=128.Turn off the switches and add the values of the off switches to obtain the correct node address.  (off=1, on=0)

| BASE I/O ADDRESS SELECTION | | | | | | |
|---|---|---|---|---|---|---|
| Address | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 |
| 2E0h | Off | On | Off | Off | Off | On |
| 2F0h | Off | On | Off | Off | Off | Off |
| 300h | Off | Off | On | On | On | On |

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| CONTROLLER INTERRUPT SELECTION | | | | | |
|---|---|---|---|---|---|
| IRQ | JP1 | JP2 | JP3 | JP4 | JP5 |
| 2 | Open | Open | Open | Open | Closed |
| 3 | Open | Open | Open | Closed | Open |
| 4 | Open | Open | Closed | Open | Open |
| 5 | Open | Closed | Open | Open | Open |
| 7 | Closed | Open | Open | Open | Open |

| SHARED RAM CONFIGURATION | | | | |
|---|---|---|---|---|
| Address | SW1/7 | SW1/8 | SW1/9 | SW1/10 |
| C000h | Off | Off | On | On |
| D000h | Off | Off | On | Off |
| E000h | Off | Off | Off | On |

| ROM ADDRESS | | | |
|---|---|---|---|
| Address | JP18 | JP19 | JP20 |
| í xC000h | Closed | Open | Open |
| x4000h | Open | Open | Closed |
| x8000h | Open | Closed | Open |
| Disabled | Open | Open | Open |

| SERVER & BRIDGE CONFIGURATION TYPES | | | |
|---|---|---|---|
| Configuration | IRQ | Base I/O Address | ROM Address |
| 0 | 2 | 2E0h | D000h |
| 1 | 2 | 2F0h | D000h |
| 2 | 3 | 2E0h | D000h |
| 3 | 3 | 2F0h | D000h |
| 4 | 4 | 2E0h | D000h |
| 5 | 4 | 2F0h | D000h |
| 6 | 5 | 2E0h | D000h |
| 7 | 5 | 2F0h | D000h |
| 8 | 7 | 2E0h | D000h |
| 9 | 7 | 2F0h | D000h |
| 10 | 2 | 300h | C000h |
| 11 | 3 | 2F0h | C000h |
| 12 | 4 | 2E0h | C000h |
| 13 | 5 | 2E0h | C000h |
| 14 | 7 | 300h | D000h |
| 15 | 2 | 300h | E000h |
| 16 | 3 | 2F0h | E000h |
| 17 | 4 | 2E0h | E000h |

**Chapter 5: Jumper Settings**

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| 8253 (TIME OUT 0) INTERRUPT SELECTION | | | | | |
|---|---|---|---|---|---|
| IRQ | JP6 | JP7 | JP8 | JP9 | JP10 |
| 2 | Open | Open | Open | Open | Closed |
| 3 | Open | Open | Open | Closed | Open |
| 4 | Open | Open | Closed | Open | Open |
| 5 | Open | Closed | Open | Open | Open |
| 7 | Closed | Open | Open | Open | Open |

| 8253 (TIME OUT 2) INTERRUPT SELECTION | | | | | |
|---|---|---|---|---|---|
| IRQ | JP11 | JP12 | JP13 | JP14 | JP15 |
| 2 | Open | Open | Open | Open | Closed |
| 3 | Open | Open | Open | Closed | Open |
| 4 | Open | Open | Closed | Open | Open |
| 5 | Open | Closed | Open | Open | Open |
| 7 | Closed | Open | Open | Open | Open |

| EXPANSION RAM CONFIGURATION | |
|---|---|
| Setting | JP21 |
| í Disabled | Open |
| Enabled | Closed |

**3**