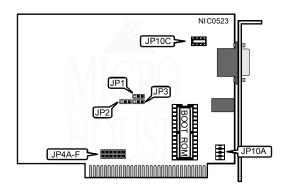
STANDARD MICROSYSTEMS CORPORATION WD8005

Token-Ring 4Mbps 8-bit ISA NIC Type Transfer Rate Data Bus Topology Ring

Unshielded twisted pair Shielded twisted pair Available Wiring Type

Boot ROM



BOOT ROM			
Setting	JP1		
íDisabled	Pins 1 & 2 closed		
Enabled	Pins 2 & 3 closed		

PRIMARY/ALTERNATE CARD			
Setting	JP2		
íPrimary	Pins 1 & 2 closed		
Alternate	Pins 2 & 3 closed		
Note: If two cards are to be used in a single system, one of the cards must be set to primary and the			

other one must be set to alternate.

BOOT ROM SIZE			
Size	JP3		
í32KB	Pins 1 & 2 closed		
64KB	Pins 2 & 3 closed		

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INTERRUPT REQUEST						
IRQ	JP4A	JP4B	JP4C	JP4D	JP4E	JP4F
í2	Closed	Open	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open	Open
5	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Open	Closed

CABLE TYPE				
Туре	JP10A	JP10C		
íUnshielded twisted pair	Installed	Open		
AUI transceiver via DB-15 port	Open	Installed		
Note: JP10A & JP10C are jumper sockets. A one-piece jumper block is installed in the sockets.				