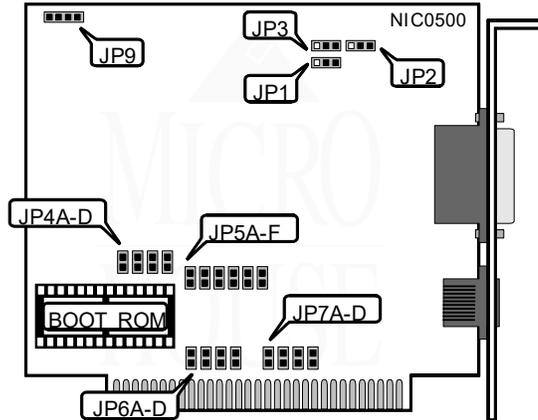


KATRON TECHNOLOGIES, INC.
 10BASE-T ETHERNET ADAPTER (8-BIT)

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	8-bit ISA
Topology	Star
Wiring Type	Unshielded twisted pair AUI transceiver via DB-15 port
Boot ROM	Available



ETHERNET VERSION	
Version	JP1
Version 2	Pins 1 & 2 closed
Version 1	Pins 2 & 3 closed

LINK INTEGRITY TEST	
Setting	JP2
Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

Note: The link integrity test is only valid when the cable type is unshielded twisted pair.

CABLE TYPE	
Type	JP3
Unshielded twisted pair	Pins 2 & 3 closed
AUI transceiver via DB-15 port	Pins 1 & 2 closed

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KATRON COMPUTERS, INC.

10BASE-T ETHERNET ADAPTER (8-BIT)

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I/O BASE ADDRESS				
Address	JP4A	JP4B	JP4C	JP4D
300h	Closed	Open	Open	Open
320h	Open	Closed	Open	Open
340h	Open	Open	Closed	Open
360h	Closed	Open	Open	Closed

BOOT ROM ADDRESS					
Address	JP5A	JP5B	JP5C	JP5D	JP5E
C0000 - C1FFFh	Pins 1 & 2				
C2000 - C3FFFh	Pins 2 & 3	Pins 1 & 2			
C4000 - C5FFFh	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
C6000 - C7FFFh	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
iC8000 - C9FFFh	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
CA000 - CBFFFh	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
CC000 - CDFFFh	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
CE000 - CFFFFh	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
D0000 - D1FFFh	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D2000 - D3FFFh	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D4000 - D5FFFh	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D6000 - D7FFFh	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D8000 - D9FFFh	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
DA000 - DBFFFh	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
DC000 - DDFFFh	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
DE000 - DFFFFh	Pins 2 & 3	Pins 1 & 2			

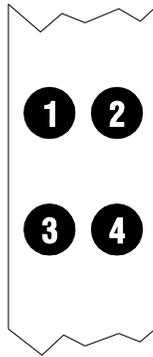
Note: Pins designated should be in the closed position.

BOOT ROM	
Setting	JP5F
iDisabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

INTERRUPT REQUEST				
IRQ	JP6A	JP6B	JP6C	JP6D
2	Open	Open	Open	Closed
i3	Open	Open	Closed	Open
4	Open	Closed	Open	Open
5	Closed	Open	Open	Open

DMA CHANNEL				
Channel	JP7A	JP7B	JP7C	JP7D
iDisabled	Open	Open	Open	Open
DMA1	Closed	Closed	Open	Open
DMA3	Open	Open	Closed	Closed

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DIAGNOSTIC LED(S)		
LED	Status	Condition
LED1	On	Twisted pair network connection is good
LED1	Off	Twisted pair is not connected/Link test disabled
LED1	Blinking	Twisted pair network connection is broken
LED2	On	Twisted pair receive wires are reversed and card has corrected
LED2	Off	Normal operation
LED3	On	Card is malfunctioning
LED3	Off	Data is not being transmitted or received
LED3	Blinking	Data is being transmitted or received
LED4	On	Collision detected on network
LED4	Off	Normal operation

Note: If the twisted pair receive wires are reversed the card will automatically detect this and correct for the reversal. It is strongly recommended that the wires be corrected as soon as possible.

CONNECTORS	
Function	Location
External Transmit/Receive LED	JP9

Note: The external TX/RX LED will function identically to LED3.