EDIMAX COMPUTER COMPANY
2000A V2.4

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Star
	Linear Bus
Wiring Type	Unshielded twisted pair
	RG-58A/U 50ohm coaxial
	AUI transceiver via DB-15 port
Boot ROM	Available
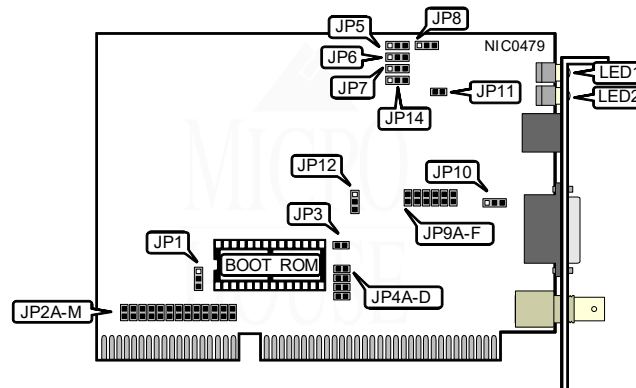
COMPATIBILITY MODE	
Setting	JP1
íDisabled	Pins 1 & 2 closed
Enabled	PIns 2 & 3 closed
Note: On some systems the data bus timing is not truly IBM PC/AT compatible. If the card is not initializing, enabling compatibility mode may allow the card to operate properly.	

INTERRUPT SETTINGS										
IRQ	JP2A	JP2B	JP2C	JP2D	JP2E	JP2F	JP2G	JP2H	JP2I	JP2J	JP2K
2/9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open

12	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
14	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open
15	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open

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I/O BASE ADDRESS		
Address	JP2L	JP2M
í300h	Closed	Closed
320h	Closed	Open
340h	Open	Closed
360h	Open	Open

BOOT ROM	
Setting	JP3
íDisabled	Open
Enabled	Closed

BOOT ROM ADDRESS				
Address	JP4A	JP4B	JP4C	JP4D
C0000h	Closed	Closed	Closed	Closed
C4000h	Closed	Closed	Closed	Open
íC8000h	Closed	Closed	Open	Closed
CC000h	Closed	Closed	Open	Open
D0000h	Closed	Open	Closed	Closed
D4000h	Closed	Open	Closed	Open
D8000h	Closed	Open	Open	Closed
DC000h	Closed	Open	Open	Open

SIGNAL QUALITY ERROR (SQE) TEST	
Setting	JP5
íEnabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed
Note: Signal Quality Error (SQE) is a test of the collision circuitry and path. Link integrity must be enabled for SQE to be enabled.	

CABLE LENGTH SELECT	
Setting	JP7
í185meters	Pins 1 & 2 closed
305meters	Pins 2 & 3 closed
Note: All cards on the network segment must have this function set the same. Link integrity must be enabled to select long cable length.	

LINK INTEGRITY	
Setting	JP8
íEnabled	Pins 2 & 3 closed
Disabled	Pins 1 & 2 closed
Note: If this option is disabled, SQE is also disabled and Cable Length is set to 185 meters	

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CABLE TYPE			
Type	JP9A - JP9F	JP10	JP12
íRG-58A/U 50ohm coaxial	Open	Pins 2 & 3 closed	Pins 2 & 3 closed
AUI transceiver via DB-15 port	Open	Pins 1 & 2 closed	Pins 1 & 2 closed
Unshielded twisted pair	Closed	Pins 2 & 3 closed	Pins 1 & 2 closed

AUTOPOLARITY	
Setting	JP14
íEnabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed
Note: When disabled, if the network wire polarities are reversed, the card will be damaged.	

FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP6	Unused

CONNECTORS	
Setting	Location
External power LED.	JP11

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Yellow	Off	Normal operation
LED1	Yellow	On	A reversed twisted pair is detected and corrected
LED2	Green	On	Twisted pair network connection is good
LED2	Green	Off	Twisted pair network connection is broken