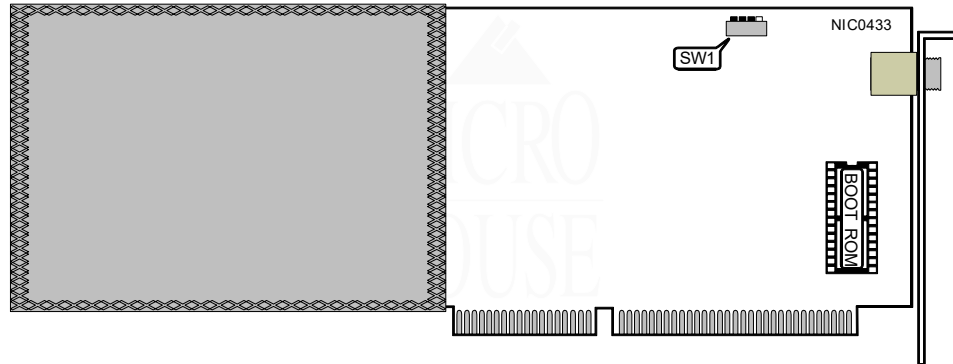


OLICOM INTERNATIONAL

OC - 3102

NIC Type Token-Ring
Transfer Rate 4Mbps
Data Bus 16-bit ISA
Topology Ring
Wiring Type Shielded twisted pair (DB-9 port)
Boot ROM Available



INTERRUPT REQUEST									
IRQ	SR3A	SR3B	SR3C	SR3D	SR3E	SR3F	SR3G	SR3H	SR3I
i2/9	Open	Open	Open	Open	Open	Closed	Open	Open	Open
3	Closed	Open	Open	Open	Open	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open	Open	Open	Open	Open
5	Open	Open	Closed	Open	Open	Open	Open	Open	Open
6	Open	Open	Open	Closed	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Open	Closed	Open	Open
11	Open	Open	Open	Open	Open	Open	Open	Closed	Open
12	Open	Open	Open	Open	Open	Open	Open	Open	Closed

DMA CHANNEL						
Channel	SR4A	SR4B	SR4C	SR4D	SR4E	SR4F
iDMA5	Closed	Open	Open	Closed	Open	Open
DMA6	Open	Closed	Open	Open	Closed	Open
DMA7	Open	Open	Closed	Open	Open	Closed

CLOCK SELECT	
Setting	SR6
Onboard clock	Pins 1 & 2 closed
System bus clock	Pins 2 & 3 closed
Note: If the system's bus clock speed is greater than 8MHz the onboard clock must be enabled.	

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BOOT ROM SIZE		
Size	SR11	SR12
16/16KB	Pins 1 & 2 closed	Pins 1 & 2 closed
32KB	Pins 2 & 3 closed	Pins 1 & 2 closed
64KB	Pins 2 & 3 closed	Pins 2 & 3 closed

BOOT ROM	
Setting	SR13
Disabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed

PRIMARY/ALTERNATE SELECT, I/O BASE ADDRESS, AND BOOT ROM ADDRESS :			
Setting	I/O Address	Boot ROM Address	SR14
Primary	0A20 - 0A2Fh	D0000 - D7FFFh	Pins 1 & 2 closed
Alternate	1A20 - 1A2Fh	D8000 - DFFFFh	Pins 2 & 3 closed
Note: If two cards are to be used in a single system, one of the cards must be set to primary and the other must be set to alternate.			

BRIDGE PAL ENABLE	
Setting	SR15
Disabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed
Note: If installing two NICs for a bridge configuration, both cards must have this option enabled and a bridge PAL must be installed on each card at UE5. This jumper is the only jumper that the Micro Channel version has.	