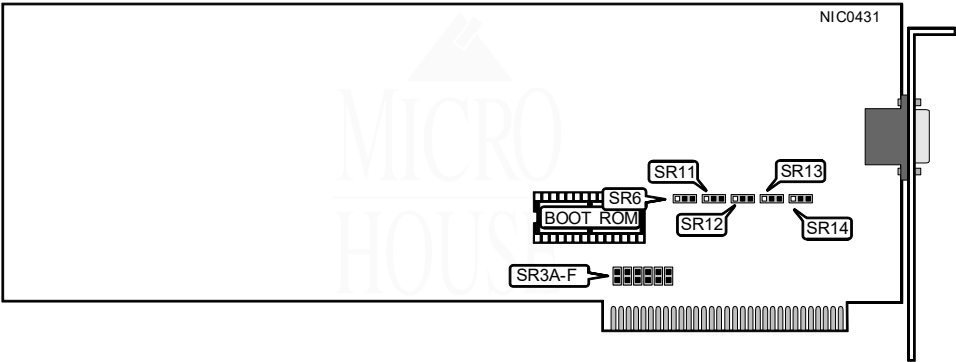


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NIC Type	Token-Ring
Transfer Rate	4Mbps
Data Bus	8-bit ISA
Topology	Ring
Wiring Type	Shielded twisted pair (DB-9 port)
Boot ROM	Available



INTERRUPT REQUEST						
IRQ	SR3A	SR3B	SR3C	SR3D	SR3E	SR3F
i2	Closed	Open	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open	Open
5	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Open	Closed

CLOCK SELECT	
Setting	SR6
iOnboard clock	Pins 1 & 2 closed
System bus clock	Pins 2 & 3 closed
Note: If the system's bus clock speed is greater than 8MHz the onboard clock must be enabled.	

BOOT ROM SIZE		
Size	SR11	SR12
i8/16KB	Pins 1 & 2 closed	Pins 1 & 2 closed
32KB	Pins 2 & 3 closed	Pins 1 & 2 closed
64KB	Pins 2 & 3 closed	Pins 2 & 3 closed

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BOOT ROM	
Setting	SR13
iDisabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed

PRIMARY/ALTERNATE SELECT, I/O BASE ADDRESS, AND BOOT ROM ADDRESS :			
Setting	I/O Address	Boot ROM Address	SR14
iPrimary	0A20 - 0A2Fh	D0000 - D7FFFh	Pins 1 & 2 closed
Alternate	1A20 - 1A2Fh	D8000 - DFFFFh	Pins 2 & 3 closed
Note: If two cards are to be used in a single system, one of the cards must be set to primary and the other must be set to alternate.			