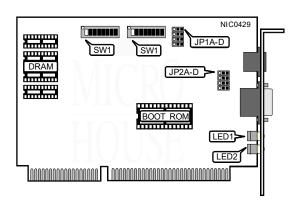
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NIC Type Token-Ring
Transfer Rate 4/16Mbps
Data Bus 8/16-bit ISA
Topology Ring

Wiring Type Unshielded twisted pair (RJ-45) Shielded twisted pair (DB-9 port)

Boot ROM Available



NETWORK SEGMENT SPEED			
Speed	JP1A - JP1D		
í16Mbps	Pins 2 & 3 closed		
4Mbps Pins 1 & 2 closed			
Note: All cards on the network segment must have this option set the same.			

CABLE TYPE		
Type	JP2A - JP2D	
íUnshielded twisted pair (RJ-45 port)	Pins 1 & 2 closed	
Shielded twisted pair (DB-9 port)	Pins 2 & 3 closed	

ONBOARD RAM SIZE				
Size	DRAM	SW1/1		
128KB	None	N/A		
512KB	(4) 256K x 4	Off		
2MB (4) 1M x 4 On				
Note: DRAMs should have an access time of 100ns.				

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BOOT ROM ADDRESS					
Address	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
C0000 -	On	On	On	On	On
C1FFFh	"	• • • • • • • • • • • • • • • • • • • •	J		"
C2000 -	Off	On	On	On	On
C3FFFh					
C4000 -	On	Off	On	On	On
C5FFFh					
C6000 -	Off	Off	On	On	On
C7FFFh					
C8000 -	On	On	Off	On	On
C9FFFh					
CA000 -	Off	On	Off	On	On
CBFFFh					
CC000 -	On	Off	Off	On	On
CDFFFh					
CE000 -	Off	Off	Off	On	On
CFFFFh					
D0000 -	On	On	On	Off	On
D1FFFh					
D2000 -	Off	On	On	Off	On
D3FFFh					
D4000 -	On	Off	On	Off	On
D5FFFh					
D6000 -	Off	Off	On	Off	On
D7FFFh					
D8000 -	On	On	Off	Off	On
D9FFFh					
DA000 -	Off	On	Off	Off	On
DBFFFh					
DC000 -	On	Off	Off	Off	On
DDFFFh					
DE000 -	Off	Off	Off	Off	On
DFFFFh				_	
E0000 -	On	On	On	On	Off
E1FFFh				_	
E2000 -	Off	On	On	On	Off
E3FFFh	0	0"	0::	0::	0"
E4000 -	On	Off	On	On	Off
E5FFFh	Off	Off	On	0-	Off
E6000 - E7FFFh	l Oii	OII	On	On	l Oii
E8000 -	On	On	Off	On	Off
E9FFFh		On	Jii	J On	
E9FFFII EA000 -	Off	On	Off	On	Off
EBFFFh		OII		J OII	
EC000 -	On	Off	Off	On	Off
EDFFFh		Oli		J OII	
EE000 -	Off	Off	Off	On	Off
EFFFFh	Oii	Oii		Oii	Oii
	<u> </u>			1	

Chapter 5: Jumper Settings

F0000 - F1FFFh	On	On	On	Off	Off
F2000 - F3FFFh	Off	On	On	Off	Off
F4000 - F5FFFh	On	Off	On	Off	Off
F6000 - F7FFFh	Off	Off	On	Off	Off
F8000 - F9FFFh	On	On	Off	Off	Off
FA000 - FBFFFh	Off	On	Off	Off	Off
FC000 - FFFFFh	On	Off	Off	Off	Off
FE000 - FFFFFh	Off	Off	Off	Off	Off

BOOT ROM		
Setting SW1/8		
íDisabled	On	
Enabled	Off	

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K1601

settings.

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DMA CHANNEL			
Channel	SW2/1	SW2/2	
íDMA5	On	On	
DMA6	Off	On	
DMA7 On Off			
Programmed I/O Off Off Off			
Note: If the card is installed into an 8-bit slot, programmed I/O is automatically selected regardless of the switch			

INTERRUPT REQUEST IRQ SW2/3 SW2/4 SW2/5 í2/9 On On Off 3 Off On Off 5 On Off On 7 Off Off On 10 On Off Off 11 Off Off Off 12 On On On 15 Off On On

I/O BASE ADDRESS				
Address	SW2/6	SW2/7	SW2/8	
0A20 - 0A23h, 0A30 - 0A3Fh	Off	Off	Off	
0A24 - 0A27h, 0A40 - 0A47h	Off	On	Off	
0A50 - 0A53h, 0A60 - 0A6Fh	On	Off	Off	
0A54 - 0A57h, 0A70 - 0A7Fh	On	On	Off	
1A20 - 1A23h, 1A30 - 1A3Fh	Off	Off	On	
1A24 - 1A27h, 1A40 - 1A47h	Off	On	On	
1A50 - 1A54h, 1A60 - 1A6Fh	On	Off	On	
1A54 - 1A57h, 1A70 - 1A7Fh	On	On	On	

FACTORY CONFIGURED SETTINGS		
Switch Setting		
SW1/2	Unused	

DIAGNOSTIC LED(S)				
LED	Color	Status	Condition	
LED1	Green	On	Network connection is good	
LED1	Green	Off	Network connection is broken	
LED2	Red	On	Data is being transmitted or received	
LED2	Red	Off	Data is not being transmitted or received	