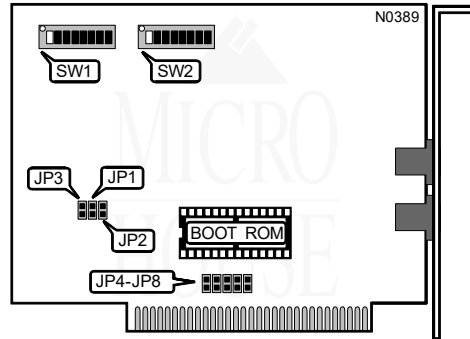


TOP MICROSYSTEMS, INC.

TOP/ARC-30 TWISTED PAIR

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 8-bit ISA
Topology Star
 Linear bus
Wiring Type Unshielded twisted pair
Boot ROM Available



NODE ADDRESS								
Node	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
0	-	-	-	-	-	-	-	-
1	Off	On	On	On	On	On	On	On
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off	Off	Off	Off	Off	Off	Off
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used.
 A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node address. (On=0, Off=1)

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TOP MICROSYSTEMS, INC.

TOP/ARC-30 TWISTED PAIR

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CABLE LENGTH AND RESPONSE/RECONFIGURATION TIMEOUTS				
Cable Length	Response Time	Reconfiguration Time	JP1	JP2
≤ 20,000 feet	74.7μs	840ms	Open	Open
> 20,000 feet	283.4μs	1680ms	Closed	Open
> 20,000 feet	561.8μs	1680ms	Open	Closed
> 20,000 feet	1118.6μs	1680ms	Closed	Closed

Note: All NICs on the network must have this option set the same.

BOOT ROM	
Setting	JP3
Disabled	Open
Enabled	Closed

Note: A 2732 or 2764 boot ROM may be used in the boot ROM socket. If a 2732 is used, the chip must be installed to the far right of the socket with the chip's notch to the left.

INTERRUPT REQUEST					
IRQ	JP4	JP5	JP6	JP7	JP8
2	Closed	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed

I/O BASE ADDRESS			
A dress	S V1/1	S V1/2	S V1/3
260-26Fh	On	On	On
290-29Fh	On	On	Off
2E0-2EFh	On	Off	On
2F0-2FFh	On	Off	Off
300-30Fh	Off	On	On
350-35Fh	Off	On	Off
380-38Fh	Off	Off	On
3E0-3EFh	Off	Off	Off

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TOP MICROSYSTEMS, INC.
TOP/ARC-30 TWISTED PAIR

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BASE MEMORY ADDRESS & BOOT ROM ADDRESS						
ase Address	Boot ROM	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
C0000h	C2000h	On	On	On	On	On
C0800h	C2000h	On	On	On	On	Off
C1000h	C2000h	On	On	On	Off	On
C1800h	C2000h	On	On	On	Off	Off
C4000h	C6000h	On	On	Off	On	On
C4800h	C6000h	On	On	Off	On	Off
C5000h	C6000h	On	On	Off	Off	On
C5800h	C6000h	On	On	Off	Off	Off
CC000h	CE000h	On	Off	On	On	On
CC800h	CE000h	On	Off	On	On	Off
CD000h	CE000h	On	Off	On	Off	On
CD800h	CE000h	On	Off	On	Off	Off
iD0000h	D2000h	On	Off	Off	On	On
D0800h	D2000h	On	Off	Off	On	Off
D1000h	D2000h	On	Off	Off	Off	On
D1800h	D2000h	On	Off	Off	Off	Off
D4000h	D6000h	Off	On	On	On	On
D4800h	D6000h	Off	On	On	On	Off
D5000h	D6000h	Off	On	On	Off	On
D5800h	D6000h	Off	On	On	Off	Off
D8000h	DA000h	Off	On	Off	On	On
D8800h	DA000h	Off	On	Off	On	Off
D9000h	DA000h	Off	On	Off	Off	On
D9800h	DA000h	Off	On	Off	Off	Off
DC000h	DE000h	Off	Off	On	On	On
DC800h	DE000h	Off	Off	On	On	Off
DD000h	DE000h	Off	Off	On	Off	On
DD800h	DE000h	Off	Off	On	Off	Off
E0000h	E2000h	Off	Off	Off	On	On
E0800h	E2000h	Off	Off	Off	On	Off
E1000h	E2000h	Off	Off	Off	Off	On
E1800h	E2000h	Off	Off	Off	Off	Off

MISCELLANEOUS NOTES

This card may be used in a star topology by terminating one of the RJ-11 jacks.