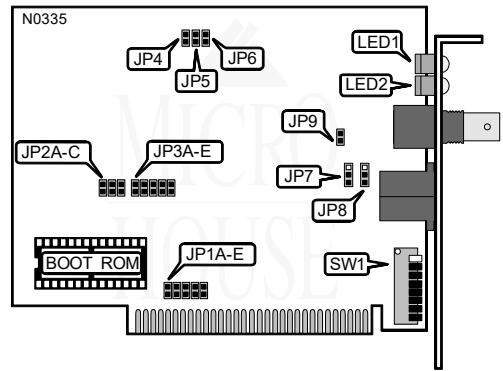


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ANET - 1 A

NIC Type	ARCnet
Transfer Rate	2.5Mbps
Data Bus	8-bit ISA
Topology	Star
	Linear Bus
Wiring Type	RG-62A/U 93ohm coaxial
	Unshielded twisted pair
Boot ROM	Available



NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	Off	Off	Off	Off	Off	Off	On
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	On	On	On	On	On	On	Off
255	On	On	On	On	On	On	On	On
<p>Note: Node address 0 is used for messaging between nodes and must not be used.</p> <p>A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 8=1, 7=2, 6=4, 5=8, 4=16, 3=32, 2=64, 1=128. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, off=0)</p>								

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AN ET - 1 A

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INTERRUPT REQUEST					
IRQ	JP1A	JP1B	JP1C	JP1D	JP1E
i2/9	Closed	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed

I/O BASE ADDRESS			
Address	JP2A	JP2B	JP2C
260h	Closed	Closed	Closed
290h	Closed	Closed	Open
i2E0h	Closed	Open	Closed
2F0h	Closed	Open	Open
300h	Open	Closed	Closed
350h	Open	Closed	Open
380h	Open	Open	Closed
3E0h	Open	Open	Open

BASE MEMORY ADDRESS & BOOT ROM ADDRESS						
Base Address	Boot ROM Address	JP3A	JP3B	JP3C	JP3D	JP3E
C0000h	C2000h	Closed	Closed	Closed	Closed	Closed
C4000h	C6000h	Closed	Closed	Open	Closed	Closed
CC000h	CE000h	Closed	Open	Closed	Closed	Closed
iD0000h	D2000h	Closed	Open	Open	Closed	Closed
D4000h	D6000h	Open	Closed	Closed	Closed	Closed
D8000h	DA000h	Open	Closed	Open	Closed	Closed
DC000h	DE000h	Open	Open	Closed	Closed	Closed
E0000h	E2000h	Open	Open	Open	Closed	Closed

WAIT STATE	
Setting	JP4
iSystem wait states	Open
Zero wait states	Closed

Note: If 0 wait states is selected, I/O CHR DY must be disabled (JP5 open).

I/O CHR DY	
Setting	JP5
iDisabled	Open
Enabled	Closed

Note: If a boot ROM is installed, I/O CHR DY must be enabled (JP5 closed).

If I/O CHR DY is enabled (JP5 closed), System wait states must be selected (JP4 open)

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ANET - 1 A

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BOOT ROM	
Setting	JP6
iDisabled	Open
Enabled	Closed

CABLE TYPE		
Type	JP7	JP8
iRG-58A/U 50ohm coaxial	Pins 1 & 2 Closed	Pins 1 & 2 Closed
Unshielded twisted pair	Pins 2 & 3 Closed	Pins 2 & 3 Closed

TOPOLOGY	
Topology	JP9
iStar	Closed
Linear Bus	Open

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Red	On	Network connection is good
LED1	Red	Blinking	Network connection is broken or no activity is detected
LED2	Green	On	Data is being received
LED2	Green	Off	Data is not being received
LED2	Green	Blinking	System power has just been applied