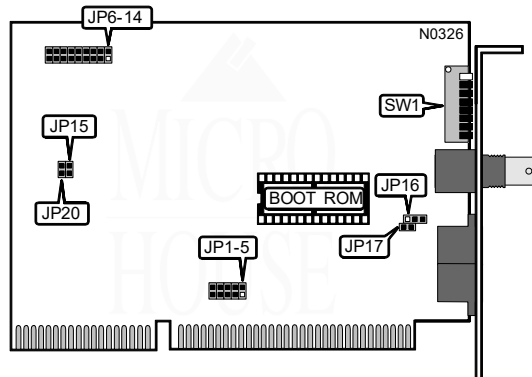


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NIC Type	ARCnet
Transfer Rate	2.5Mbps
Data Bus	16-bit ISA
Topology	Star
Wiring Type	Linear Bus
	Shielded/Unshielded twisted pair
	RG-62A/U 93ohm coaxial
Boot ROM	Available



NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	On	On	On	On	On	On	On
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off	Off	Off	Off	Off	Off	Off
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used.
A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node address. (On=0, off=1)

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INTERRUPT REQUEST					
IRQ	JP1	JP2	JP3	JP4	JP5
i2/9	Open	Open	Open	Open	Closed
3	Open	Open	Open	Closed	Open
4	Open	Open	Closed	Open	Open
5	Open	Closed	Open	Open	Open
7	Closed	Open	Open	Open	Open

I/O ADDRESS			
Address	JP6	JP7	JP8
260h	Closed	Closed	Closed
290h	Closed	Closed	Open
i2E0h	Closed	Open	Closed
2F0h	Closed	Open	Open
300h	Open	Closed	Closed
350h	Open	Closed	Open
380h	Open	Open	Closed
3E0h	Open	Open	Open

Note: Jumpers count from right to left in the diagram.

BOOT ROM	
Setting	JP14
iDisabled	Open
Enabled	Closed

BASE ADDRESS AND BOOT ROM ADDRESS						
Address	Boot ROM Address	JP9	JP10	JP11	JP12	JP13
C0000h	C2000h	Closed	Closed	Closed	Closed	Closed
C4000h	C6000h	Closed	Closed	Open	Closed	Closed
CC000h	CE000h	Closed	Open	Closed	Closed	Closed
D0000h	D2000h	Closed	Open	Open	Closed	Closed
D4000h	D6000h	Open	Closed	Closed	Closed	Closed
D8000h	DA000h	Open	Closed	Open	Closed	Closed
DC000h	DE000h	Open	Open	Closed	Closed	Closed
E0000h	E2000h	Open	Open	Open	Closed	Closed

Note: Jumpers count from right to left in the diagram.

WAIT STATE	
Setting	JP15
iZero wait states disabled	Open
Zero wait states enabled	Closed

Note: If Zero wait states are enabled, IOCHRDY (JP20) must be disabled.

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CABLE TYPE			
Type	Topology	JP16	JP17
RG62A/U 93ohm with terminator	Star	Pins 1 & 2 Closed	Closed
RG62A/U 93ohm without terminator	Bus	Open	Closed
Shielded/Unshielded twisted pair	Bus	Pins 2 & 3 Closed	Open

I/O CHANNEL READY	
Status	JP20
IOCHRDY signal disabled	Open
IOCHRDY signal enabled	Closed
Note: If IOCHRDY signal is enabled, Zero wait state (JP15) must be disabled. If optional PROM is installed, IOCHRDY must be enabled and Zero wait state (JP15) must be disabled.	