COMPEX, INC.
ANET16-1, ANET16-12

NIC Type	ARCnet
Transfer Rate	2.5Mbps
Data Bus	16-bit ISA
Topology	Star (ANET16-1)
	Linear Bus (ANET16-12)
Wiring Type	RG-62A/U 93ohm coaxial
Boot ROM	Available
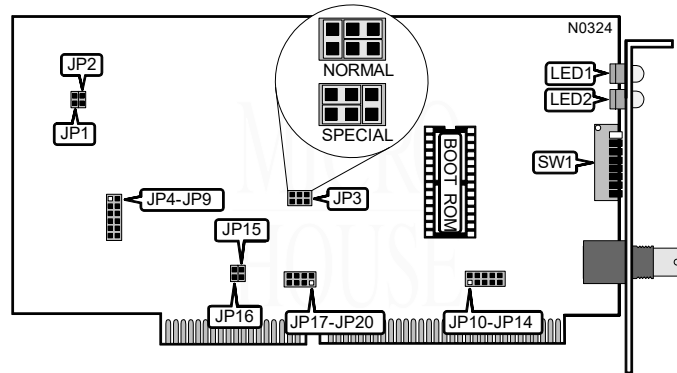
NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	On	On	On	On	On	On	On
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off	Off	Off	Off	Off	Off	Off
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used.

A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node address. (On=0, off=1)

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DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Red	On	Data is being transmitted
LED1	Red	Off	Data is not being transmitted
LED2	Green	On	Data is being received
LED2	Green	Off	Data is not being received

TIMEOUT CONFIGURATION		
Response Time	JP1	JP2
í74.7μs	Open	Open
283.4μs	Open	Closed
561.8μs	Closed	Open
1118.6μs	Closed	Closed
Note: Timeout is the time required for the network signal to make a complete trip around the network. Using a longer than necessary timeout will result in degradation of network performance.		

MODE SELECTION	
Mode	JP3
íNormal	As shown in diagram on previous page
Special	As shown on diagram on previous page
Note: In normal mode, any 32KB memory segment in the system may be selected. For an AT, however, with memory, display cards, and a hard drive controller, only 16KB of free memory space is available between CC000h and CFFFFh. In Special Mode the memory space occupied by the adapter is reduced by half to 16KB and the memory address is automatically set to CC000h. This overrides the base memory setting on JP15 and JP16.	

I/O BASE ADDRESS						
Address	JP4	JP5	JP6	JP7	JP8	JP9
260h	Closed	Open	Open	Closed	Closed	Open
280h	Closed	Closed	Closed	Open	Closed	Open
í2E0h	Closed	Open	Open	Open	Closed	Open
2F0h	Open	Open	Open	Open	Closed	Open
300h	Closed	Closed	Closed	Closed	Open	Open
360h	Closed	Open	Open	Closed	Open	Open

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INTERRUPT REQUEST									
IRQ	JP10	JP11	JP12	JP13	JP14	JP17	JP18	JP19	JP20
í2/9	Closed	Open	Open	Open	Open	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open	Open	Open	Open	Open
5	Open	Open	Open	Closed	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Open	Open	Open	Closed
Note: Jumpers JP17 to JP20 count from right to left in the diagram.									

BOOT ROM ADDRESS		
Address	JP15	JP16
C0000h	Closed	Closed
C8000h	Open	Closed
CC000h	Open	Open
íD0000h	Closed	Open