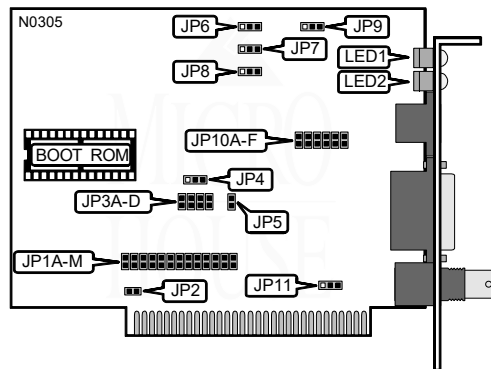


EDIMAX COMPUTER COMPANY

1000A, EN-7008 V1.0A

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	8-bit ISA
Topology	Star
	Linear Bus
Wiring Type	RG-58A/U 50ohm coaxial
	Unshielded twisted pair
	AUI transceiver via DB-15 port
Boot ROM	Available



I/O BASE ADDRESS			
Address	JP1A	JP1B	JP1C
i300h	Closed	Closed	Closed
320h	Closed	Closed	Open
340h	Closed	Open	Closed
360h	Closed	Open	Open

INTERRUPT SETTINGS						
IRQ	JP1D	JP1E	JP1F	JP1G	JP1H	JP1I
2	Closed	Open	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open	Open
5	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Open	Closed

DMA CHANNEL				
Channel	JP1J	JP1K	JP1L	JP1M
DMA1	Closed	Open	Closed	Open
DMA3	Open	Closed	Open	Closed

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CONNECTORS	
Setting	Jumper
External power LED.	JP2

BOOT ROM ADDRESS				
Address	JP3A	JP3B	JP3C	JP3D
C0000h	Closed	Closed	Closed	Closed
C4000h	Open	Closed	Closed	Closed
iC8000h	Closed	Open	Closed	Closed
CC000h	Open	Open	Closed	Closed
D0000h	Closed	Closed	Open	Closed
D4000h	Open	Closed	Open	Closed
D8000h	Closed	Open	Open	Closed
DC000h	Open	Open	Open	Closed

CABLE TYPE			
Type	JP4	JP10A-JP10F	JP11
iRG-58A/U 50ohm coaxial	Pins 1 & 2 closed	Open	Pins 2 & 3 closed
AUI transceiver via DB-15 port	Pins 2 & 3 closed	Open	Pins 1 & 2 closed
Unshielded twisted pair	Pins 2 & 3 closed	Closed	Pins 2 & 3 closed

BOOT ROM	
Setting	JP5
iDisabled	Open
Enabled	Closed

AUTOPOLARITY	
Setting	JP6
iEnabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed
Note: When disabled, if the wires' polarities are reversed, the card may be damaged.	

SIGNAL QUALITY ERROR (SQE) TEST	
Setting	JP7
iEnabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed
Note: Signal Quality Error (SQE) is a test of the collision circuitry and path.	

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SEGMENT LENGTH	
Maximum Length	JP8
i185 meters	Pins 2 & 3 closed
300 meters	Pins 1 & 2 closed
Note: Segment length is the total length of cable between the two farthest cards on the segment. Each card on the segment must have this option set the same.	

LINK INTEGRITY TEST	
Setting	JP9
iEnabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Yellow	On	A reversed twisted pair is detected and corrected
LED1	Yellow	Off	Normal operation
LED2	Green	On	Normal operation
LED2	Green	Off	A reversed twisted pair is detected and corrected

MISCELLANEOUS NOTES
The EN-7008 is Novell certified, and the 1000A is not. Chips may be in different locations on different revisions of the boards.