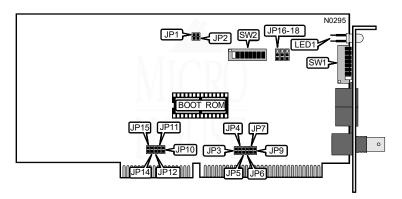
## CNET TECHNOLOGY, INC. CN190SBT

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 16-bit ISA
Topology Star
Linear Bus

Wiring Type Unshielded twisted pair RG-62A/U 93ohm coaxial

Boot ROM Available



	NODE ADDRESS							
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	On						
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off						
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used. A total of 25t node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node address. (On=0, off=1)

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RESPONSE/RECONFIGURATION TIMEOUTS					
Response Time	Reconfiguration Time	JP1	JP2		
74.7μs	840ms	Open	Open		
283.4μs	1680ms	Closed	Open		
561.8μs	1680ms	Open	Closed		
1118.6μs 1680ms Closed Closed					
Note: All NICs on the network segment must have this option set the same.					

				II	NTERRUF	T REQUE	ST				
IRQ	JP3	JP4	JP5	JP6	JP7	JP9	JP10	JP11	JP12	JP14	JP15
í3	Close d	Open									
4	Open	Close d	Open								
5	Open	Open	Close d	Open							
6	Open	Open	Open	Close d	Open						
7	Open	Open	Open	Open	Close d	Open	Open	Open	Open	Open	Open
9	Open	Open	Open	Open	Open	Close d	Open	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Open	Close d	Open	Open	Open	Open
11	Open	Close d	Open	Open	Open						
12	Open	Close d	Open	Open							
14	Open	Close d	Open								
15	Open	Close d									

CABLE	TYPE
Туре	JP16 & JP17
íUnshielded twisted pair	Pins 1 & 2 closed
RG-62A/U 93ohm coaxial	Pins 2 & 3 closed

	ONBOARD TERMINATOR				
	Setting	JP18			
	íDisabled	Pins 2 & 3 closed			
	Enabled	Pins 1 & 2 closed			
Notes:	If the card is on either end of a linear bus i	network segment, the onboard terminator may be used			
	instead of using an external terminator.				

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	I/O BASE ADDRESS					
Address	SW2/1	SW2/2	SW2/3	SW2/4		
200h	On	On	On	On		
220h	On	On	On	Off		
240h	On	On	Off	On		
260h	On	On	Off	Off		
280h	On	Off	On	On		
2A0h	On	Off	On	Off		
2C0h	On	Off	Off	On		
2E0h	On	Off	Off	Off		
300h	Off	On	On	On		
320h	Off	On	On	Off		
340h	Off	On	Off	On		
360h	Off	On	Off	Off		
380h	Off	Off	On	On		
3A0h	Off	Off	On	Off		
3C0h	Off	Off	Off	On		
3E0h	Off	Off	Off	Off		

	BASE MEMORY ADDRE	ESS & BOOT R	OM ADDRESS		
Base Address	Boot ROM Address	SW2/5	SW2/6	SW2/7	SW2/8
C0000h	C4000h	Off	On	On	On
C8000h	CC000h	Off	On	On	Off
íD0000h	D4000h	Off	On	Off	On
D8000h	DC000h	Off	On	Off	Off
E0000h	E4000h	Off	Off	On	On

DIAGNOSTIC LED(S)				
LED	Status	Condition		
LED1	On	Data is being transmitted or received		
LED1	Off	Data is not being transmitted or received		

FACTORY CONFIGURED SETTINGS			
Jumper	Setting		
JP1	Open		
JP2	Open		