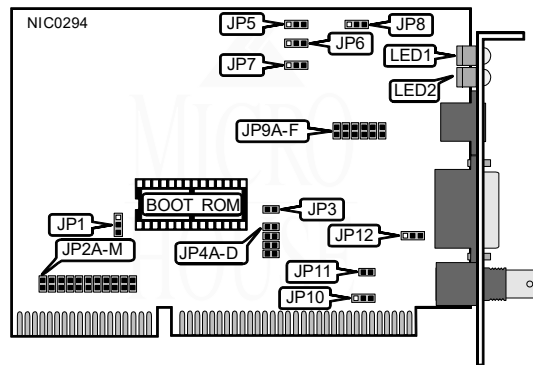


EDIMAX COMPUTER COMPANY

2000A

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Star
	Linear Bus
Wiring Type	RG-58A/U 50ohm coaxial
	Unshielded twisted pair
	AUI transceiver via DB-15 port
Boot ROM	Available



COMPATIBILITY MODE	
Setting	JP1
iDisabled	Pins 1 & 2 Closed
Enabled	Pins 2 & 3 Closed

Note: On some systems the data bus timing is not truly IBM PC/AT compatible. If the card is not initializing, enabling compatibility mode may allow the card to operate properly.

INTERRUPT SETTINGS											
IRQ	JP2A	JP2B	JP2C	JP2D	JP2E	JP2F	JP2G	JP2H	JP2I	JP2J	JP2K
2	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
i3	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open

Chapter 5: Jumper Settings

12	Open	Open	Close d	Open	Open	Open	Open	Open	Open	Open	Open
14	Open	Close d	Open	Open	Open	Open	Open	Open	Open	Open	Open
15	Close d	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open

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BOOT ROM	
Setting	JP3
Disabled	Open
Enabled	Closed

I/O BASE ADDRESS		
Address	JP2L	JP2M
300h	Closed	Closed
320h	Closed	Open
340h	Open	Closed
360h	Open	Open

BOOT ROM ADDRESS				
Address	JP4A	JP4B	JP4C	JP4D
C000h	Closed	Closed	Closed	Closed
C400h	Closed	Closed	Closed	Open
C800h	Closed	Closed	Open	Closed
CC00h	Closed	Closed	Open	Open
D000h	Closed	Open	Closed	Closed
D400h	Closed	Open	Closed	Open
D800h	Closed	Open	Open	Closed
DC00h	Closed	Open	Open	Open

AUTO-POLARITY	
Setting	JP5
Enabled	Pins 1 & 2 Closed
Disabled	Pins 2 & 3 Closed

Note: When disabled, if a reversed polarity signal occurs the card may be damaged.

SIGNAL QUALITY ERROR (SQE) TEST	
Setting	JP6
Enabled	Pins 1 & 2 Closed
Disabled	Pins 2 & 3 Closed

Note: Signal Quality Error (SQE) is a test of the collision circuitry and path.

SEGMENT LENGTH	
Maximum Length	JP7
185 meters	Pins 2 & 3 closed
300 meters	Pins 1 & 2 closed

Note: Segment length is the total length of cable between the two farthest cards on the segment. All cards on the network segment must have this option set the same.

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LINK INTEGRITY TEST	
Setting	JP8
Enabled	Pins 1 & 2 Closed
Disabled	Pins 2 & 3 Closed
Note: The link integrity test is only valid when the cable type is unshielded twisted pair.	

CABLE TYPE			
Type	JP9A-JP9F	JP10	JP12
1RG-58A/U 50ohm coaxial	Open	Pins 2 & 3 Closed	Pins 1 & 2 Closed
AUI transceiver via DB-15 port	Open	Pins 1 & 2 Closed	Pins 2 & 3 Closed
Shielded/Unshielded twisted pair	Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Yellow	On	A reversed twisted pair is detected and corrected
LED1	Yellow	Off	Normal operation
LED2	Green	On	Normal operation
LED2	Green	Off	A reversed twisted pair is detected and corrected