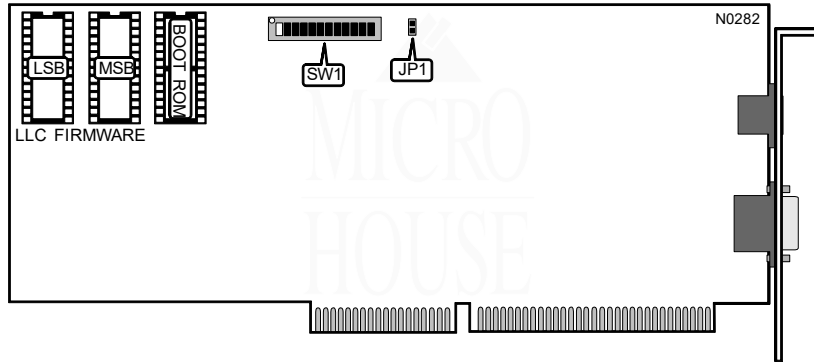


# ZENITH DATA SYSTEMS

## LAN 16TR-AT

**NIC Type** Token-Ring  
**Transfer Rate** 4/16Mbps  
**Data Bus** 16-bit ISA  
**Topology** Ring  
**Wiring Type** Unshielded twisted pair  
                   Shielded twisted pair  
**Boot ROM** Available



FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP1	Unused

I/O BASE ADDRESS		
Address	SW1/1	SW1/2
A00 - A0Fh	On	On
A20 - A2Fh	Off	On
A40 - A4Fh	On	Off
A60 - A6Fh	Off	Off

INTERRUPT REQUEST		
IRQ	SW1/3	SW1/4
3	On	On
9	Off	On
10	On	Off
11	Off	Off

BOOT ROM ADDRESS		
Address	SW1/5	SW1/6
CC000 - CFFFFh	On	On
D0000 - D3FFFh	Off	On
D8000 - D8FFFh	On	Off
DC000 - DFFFFh	Off	Off

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DMA CHANNEL		
Channel	SW1/7	SW1/8
DMA0	On	On
DMA5	Off	On
DMA6	On	Off
DMA7	Off	Off

BOOT ROM	
Setting	SW1/9
Disabled	On
Enabled	Off

NETWORK SEGMENT SPEED	
Speed	SW1/10
4Mbps	Off
16Mbps	On
Note: All cards on the network segment must have this option set the same.	

LLC EPROM CONFIGURATION	
Setting	SW1/11
iROM-based LLC	On
RAM-based LLC	Off
Note: The LLC EPROMs contain the Logic Link Control program. With the LLC ROMs disabled, the LLC logic can be loaded into RAM. The card will function properly with the LLC chips disabled.	

CABLE TYPE	
Type	SW1/12
Unshielded twisted pair (RJ-45 Jack)	Off
Shielded twisted pair (DB-9 Connector)	On