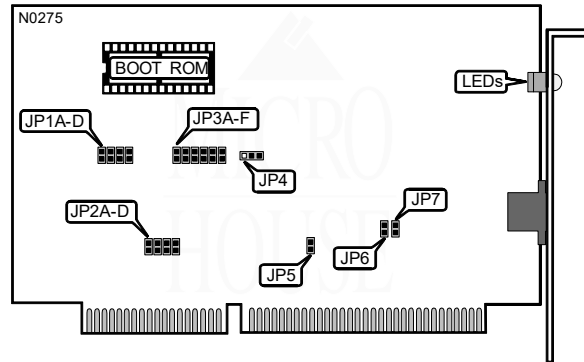


ZENITH DATA SYSTEMS

S Q - 5 5 5 0 / 5 5 5 1

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Star
Wiring Type	Shielded/Unshielded twisted pair
Boot ROM	Available



DMA CHANNEL								
Channel	JP1A	JP1B	JP1C	JP1D	JP2A	JP2B	JP2C	JP2D
DMA3	Open	Open	Open	Closed	Open	Open	Open	Closed
iDMA5	Open	Open	Closed	Open	Open	Open	Closed	Open
DMA6	Open	Closed	Open	Open	Open	Closed	Open	Open
DMA7	Closed	Open	Open	Open	Closed	Open	Open	Open

INTERRUPT REQUEST						
IRQ	JP3A	JP3B	JP3C	JP3D	JP3E	JP3F
i3	Closed	Open	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open	Open
5	Open	Open	Closed	Open	Open	Open
9	Open	Open	Open	Closed	Open	Open
10	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Closed

DATA I/O MODE	
Setting	JP4
i8-bit mode	Pins 2 & 3 Closed
16-bit mode	Pins 1 & 2 Closed
Note: Card must be installed in a 16-bit slot. This setting controls the size of the data bus used. Memory accesses always use 16-bit words.	

Continued on next page . . .

ZENITH DATA SYSTEMS

SQ-5550/5551

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BOOT ROM	
Setting	JP5
Disabled	Open
Enabled	Closed

I/O BASE ADDRESS AND BOOT ROM ADDRESS			
Address	ROM Address	JP6	JP7
300h	C8000h	Closed	Closed
320h	CC000h	Closed	Open
340h	D0000h	Open	Closed
360H	D4000h	Open	Open



DIAGNOSTIC LED(S)		
LED	Status	Condition
1	On	Data is being transmitted
1	Off	Data is not being transmitted
2	Bright	Data is being received
2	Dim	Link pulses are being received
2	Off	Data is not being received