PURE DATA LTD.
PDI9025-8

NIC Type	Token-Ring
Transfer Rate	4/16Mbps
Data Bus	8-bit ISA
Topology	Ring
Wiring Type	Shielded/Unshielded twisted pair
Boot ROM	Available
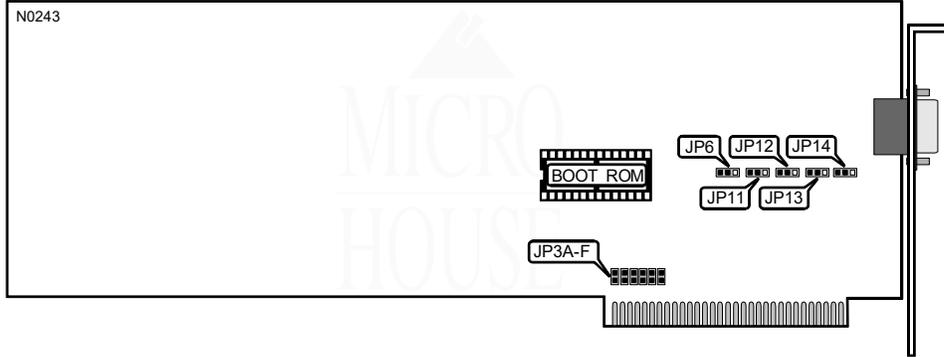
INTERRUPT REQUEST						
IRQ	JP3A	JP3B	JP3C	JP3D	JP3E	JP3F
í2	Open	Open	Open	Open	Open	Closed
3	Open	Open	Open	Open	Closed	Open
4	Open	Open	Open	Closed	Open	Open
5	Open	Open	Closed	Open	Open	Open
6	Open	Closed	Open	Open	Open	Open
7	Closed	Open	Open	Open	Open	Open

CARD TIMING	
Setting	JP6
íOscillator select internal (6MHz)	Pins 2 & 3 Closed
Oscillator select external (Host bus speed of 8 or 10MHz)	Pins 1 & 2 Closed

BOOT ROM SIZE		
ROM size	JP11	JP12
í8/16KB	Pins 2 & 3 Closed	Pins 2 & 3 Closed
32KB	Pins 1 & 2 Closed	Pins 2 & 3 Closed
64KB	Pins 1 & 2 Closed	Pins 1 & 2 Closed

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BOOT ROM	
Setting	JP13
Disabled	Pins 2 & 3 Closed
Enabled	Pins 1 & 2 Closed
Note: Location of Boot ROM socket is unknown	

ALTERNATE/PRIMARY CARD SELECT			
Setting	Address	Boot ROM Address	JP14
íPrimary	0A20-0A2Fh	D0000-D7FFFh	Pins 2 & 3 Closed
Alternate	1A20-1A2Fh	D8000-DFFFFh	Pins 1 & 2 Closed