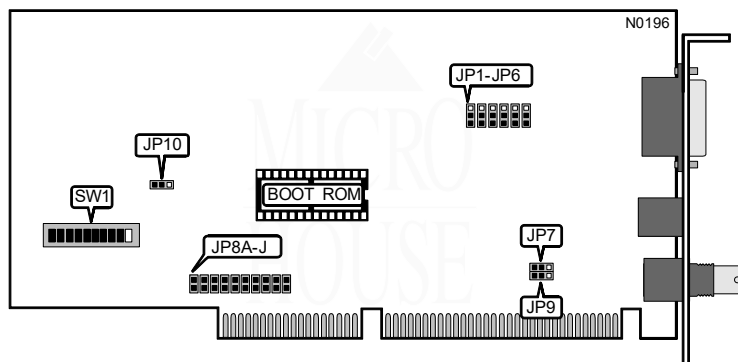


AMERICAN RESEARCH CORPORATION ETHERFLEX 16B, PCN013

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Star
	Linear Bus
Wiring Type	Unshielded twisted pair
	RG58A/U 50ohm coaxial
	DIX transceiver via DB-15 port
Boot ROM	Available



I/O BASE ADDRESS					
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5
200h	Off	On	On	On	On
220h	Off	On	On	On	Off
240h	Off	On	On	Off	On
260h	Off	On	On	Off	Off
280h	Off	On	Off	On	On
2A0h	Off	On	Off	On	Off
2C0h	Off	On	Off	Off	On
2E0h	Off	On	Off	Off	Off
300h	Off	Off	On	On	On
320h	Off	Off	On	On	Off
340h	Off	Off	On	Off	On
360h	Off	Off	On	Off	Off
380h	Off	Off	Off	On	On
3A0h	Off	Off	Off	On	Off
3C0h	Off	Off	Off	Off	On
3E0h	Off	Off	Off	Off	Off

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BOOT ROM	
Setting	SW1/6
Disabled	Off
Enabled	On

BOOT ROM ADDRESS FOR 8KB ROM SIZE				
Address	SW1/7	SW1/8	SW1/9	SW1/10
C0000h	On	On	On	On
C4000h	On	On	On	Off
C8000h	On	On	Off	On
CC000h	On	On	Off	Off
D0000h	On	Off	On	On
D4000h	On	Off	On	Off
D8000h	On	Off	Off	On
DC000h	On	Off	Off	Off
E0000h	Off	On	On	On
E4000h	Off	On	On	Off
E8000h	Off	On	Off	On
EC000h	Off	On	Off	Off
F0000h	Off	Off	On	On
F4000h	Off	Off	On	Off
F8000h	Off	Off	Off	On
FC000h	Off	Off	Off	Off

CABLE TYPE		
Type	JP1 - JP6	JP10 & JP11
Unshielded twisted pair	Pins 1 & 2 Closed	Pins 1 & 2 closed
RG-58A/U 50ohm	Pins 2 & 3 Closed	Pins 2 & 3 Closed
DIX transceiver via DB-15	Pins 2 & 3 Closed	Pins 1 & 2 closed

DMA CHANNEL				
Channel	JP8B	JP8C	JP8D	JP8E
DMA1	Open	Closed	Open	Closed
DMA3	Closed	Open	Closed	Open

INTERRUPT REQUEST				
IRQ	JP8G	JP8H	JP8I	JP8J
2/9	Open	Open	Open	Closed
3	Open	Open	Closed	Open
4	Open	Closed	Open	Open
5	Closed	Open	Open	Open

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I/O MAPPED/MEMORY MAP SELECTION	
Setting	JP10
I/O mapped	Pins 1 & 2 Closed
Memory map	Pins 2 & 3 Closed
Note: If the adapter is not working properly, try using the Memory map option.	

FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP9A	Open
JP9F	Open