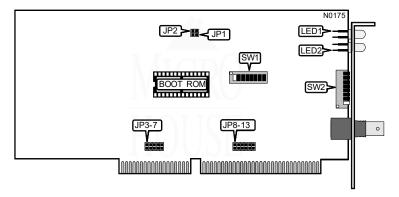
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NIC Type Transfer Rate Data Bus Topology Wiring Type Boot ROM ARCnet 2.5Mbps 16-bit ISA Star RG-62/U 93ohm coaxial Available



NODE ADDRESS								
Node	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
0	-	-	-	-	-	-	-	-
1	Off	On						
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off						
255	Off							
Note: Node address 0 is used for messaging between nodes and must not be used. A total of 255 node address settings are available. The switches are a binary repres ntation of the								
decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most 3ignificant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node ad lress. (On=0,								

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off=1)

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RESPONSE/RECONFIGURATION TIMEOUTS							
Response Time	Reconfiguration Time	JP1	JP2				
74.7μs	840ms	Open	Open				
283.4µs	1680ms	Closed	Open				
561.8μs	1680ms	Open	Closed				
1118.6μs 1680ms Closed Closed							
Note: All NICs on the network must have this option set the same.							

				I	NTERRUF	PT REQUE	EST				
IRQ	JP3	JP4	JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
í2	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Close d
3	Open	Open	Open	Open	Open	Close d	Open	Open	Open	Open	Open
4	Open	Open	Open	Open	Open	Open	Close d	Open	Open	Open	Open
5	Open	Open	Open	Open	Open	Open	Open	Close d	Open	Open	Open
7	Open	Open	Open	Open	Open	Open	Open	Open	Open	Close d	Open
10	Open	Open	Open	Open	Close d	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Close d	Open	Open	Open	Open	Open	Open	Open
12	Open	Open	Close d	Open	Open	Open	Open	Open	Open	Open	Open
15	Open	Close d	Open	Open	Open	Open	Open	Open	Open	Open	Open

	I/O BASE ADDRESS						
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	
260h	Off	On	On	Off	Off	On	
290h	Off	On	Off	On	On	Off	
í2E0h	Off	On	Off	Off	Off	On	
2F0h	Off	On	Off	Off	Off	Off	
300h	Off	Off	On	On	On	On	
350h	Off	Off	On	Off	On	Off	
380h	Off	Off	Off	On	On	On	
3E0h	Off	Off	Off	Off	Off	On	

	BASE MEMORY ADDRESS & BOOT ROM ADDRESS (REV.D, VER: A 05-01-0005-(3)						
Bas	e Address	Boot ROM Address	SW1/7	SW1/7	SW1/9	SW1/10	
(C0000h	C8000h	Off	Off	On	On	
í	0000h	D8000h	Off	Off	On	Off	
	E0000h	E8000h	Off	Off	Off	On	

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BASE MEMORY ADDRESS & BOOT ROM ADDRESSS (VER: A 05-01-0005-04 OR AB VE)							
Base Address	Boot ROM Address	SW1/7	SW1/8	SW1/9	SW1/10		
C0000h	C2000h	On	On	On	On		
C4000h	C6000h	On	On	On	Off		
CC000h	CE000h	On	On	Off	Off		
íD0000h	D2000h	On	Off	On	On		
D4000h	D6000h	On	Off	On	Off		
D8000h	DA000h	On	Off	Off	On		
DC000h	DE000h	On	Off	Off	Off		
E0000h	E2000h	Off	On	On	On		

DIAGNOSTIC LED(S)						
LED	Status	Condition				
LED1	On	Link integrity is good				
LED1	Off	Link integrity is broken				
LED2	On	Data is being transmitted or recieved				
LED2	Off	Data is not being transmitted or recieved				