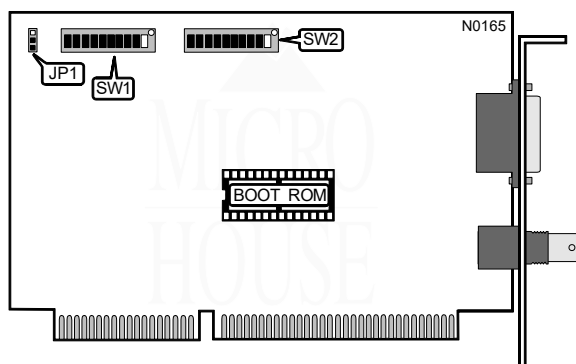


BUSLOGIC, INC.

BT - 560 A

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Linear Bus
Wiring Type	RG-58A/U 50ohm coaxial
	AUI transceiver via DB-15 port
Boot ROM	Available



I/O CHANNEL READY	
Setting	JP1
iDisabled	Pins 2 & 3 Closed
Enable	Pins 1 & 2 Closed
Note: In some cases memory may require a lengthened transfer cycle. In these cases, the BT-560A must wait for a Host I/O READY signal (set to Host I/O Channel Ready Signal to Enabled).	

CARD FUNCTION	
Setting	SW1/1
iEnabled	On
Disabled	Off

INTERRUPT REQUEST			
IRQ	SW1/2	SW1/3	SW1/4
2/9	Off	Off	On
10	On	Off	On
11	Off	On	On
i15	On	On	On

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DMA CHANNEL		
Channel	SW1/5	SW1/6
Disabled	Off	Off
DMA5	On	Off
DMA6	Off	On
iDMA7	On	On

DATA BUS TRANSFER RATE		
Setting	SW1/8	SW1/9
i5MBps	On	On
6.7MBps	On	Off
8MBps	Off	On
10MBps	Off	Off
Note: This setting specifies the rate at which the card transfers data (in Megabytes per second) on the data bus when acting as bus master.		

MEMORY STROBE-TIMING	
Setting	SW1/10
i150ns	On
100ns	Off
Note: This switch selects the amount of time between the PC/AT's memory-write or memory-read command strobes.	

BOOT ROM ADDRESS					
Address	Boot ROM	SW2/1	SW2/2	SW2/3	SW2/4
C0000h	8K or 16K	Off	Off	Off	Off
C2000h	8K	On	Off	Off	Off
C4000h	8K or 16K	Off	On	Off	Off
C6000h	8K	On	On	Off	Off
C8000h	8K or 16K	Off	Off	On	Off
CA000h	8K	On	Off	On	Off
CC000h	8K or 16K	Off	On	On	Off
CD000h	8K	On	On	On	Off
D0000h	8K or 16K	Off	Off	Off	On
D2000h	8K	On	Off	Off	On
D4000h	8K or 16K	Off	On	Off	On
D6000h	8K	On	On	Off	On
D8000h	8K or 16K	Off	Off	On	On
DA000h	8K	On	Off	On	On
DC000h	8K or 16K	Off	On	On	On
iDE000h	8K	On	On	On	On

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BOOT ROM SIZE		
Size	SW2/5	SW2/6
iDisabled	On	On
8K size	Off	On
16K size	On	Off

CABLE TYPE	
Type	SW2/7
iRG-58A/U 50ohm coaxial	On
AUI transceiver via DB-15 port	Off

I/O BASE ADDRESS			
Address	SW2/8	SW2/9	SW2/10
100h	Off	Off	Off
120h	On	Off	Off
140h	Off	On	Off
160h	On	On	Off
300h	Off	Off	On
320h	On	Off	On
i340h	On	On	On