STANDARD MICROSYSTEMS CORPORATION
P C 1 3 0 E

NIC Type	ARCnet
Transfer Rate	2.5Mbps
Data Bus	8-bit ISA
Topology	Star
	Linear Bus
Wiring Type	RG-62A/U 93ohm coaxial
Boot ROM	Available
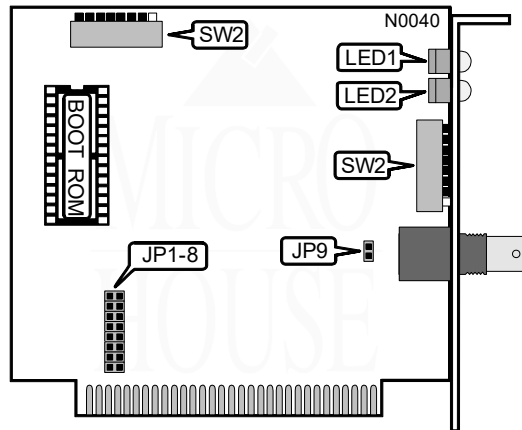
NODE ADDRESS								
Node	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
0	-	-	-	-	-	-	-	-
1	Off	On	On	On	On	On	On	On
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off	Off	Off	Off	Off	Off	Off
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used.
A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node address. (On=0, Off=1)

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INTERRUPT REQUEST					
IRQ	JP1D	JP1E	JP1F	JP1G	JP1H
2	Open	Open	Open	Open	Closed
3	Open	Open	Open	Closed	Open
4	Open	Open	Closed	Open	Open
5	Open	Closed	Open	Open	Open
7	Closed	Open	Open	Open	Open

BASE MEMORY ADDRESS & BOOT ROM ADDRESS						
Base Address	Boot ROM Address	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
C0000h	C2000h	On	On	On	On	On
C0800h	C2000h	On	On	On	On	Off
C1000h	C2000h	On	On	On	Off	On
C1800h	C2000h	On	On	On	Off	Off
C4000h	C6000h	On	On	Off	On	On
C4800h	C6000h	On	On	Off	On	Off
C5000h	C6000h	On	On	Off	Off	On
C5800h	C6000h	On	On	Off	Off	Off
CC000h	CE000h	On	Off	On	On	On
CC800h	CE000h	On	Off	On	On	Off
CD000h	CE000h	On	Off	On	Off	On
CD800h	CE000h	On	Off	On	Off	Off
D0000h	D2000h	On	Off	Off	On	On
D0800h	D2000h	On	Off	Off	On	Off
D1000h	D2000h	On	Off	Off	Off	On
D1800h	D2000h	On	Off	Off	Off	Off
D4000h	D6000h	Off	On	On	On	On
D4800h	D6000h	Off	On	On	On	Off
D5000h	D6000h	Off	On	On	Off	On
D5800h	D6000h	Off	On	On	Off	Off
D8000h	DA000h	Off	On	Off	On	On
D8800h	DA000h	Off	On	Off	On	Off
D9000h	DA000h	Off	On	Off	Off	On
D9800h	DA000h	Off	On	Off	Off	Off
DC000h	DE000h	Off	Off	On	On	On
DC800h	DE000h	Off	Off	On	On	Off
DD000h	DE000h	Off	Off	On	Off	On
DD800h	DE000h	Off	Off	On	Off	Off
E0000h	E2000h	Off	Off	Off	On	On
E0800h	E2000h	Off	Off	Off	On	Off
E1000h	E2000h	Off	Off	Off	Off	On
E1800h	E2000h	Off	Off	Off	Off	Off

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ARCnet PC130E

RESPONSE AND RECONFIGURATION TIMEOUTS				
Response Time	Idle Time	Reconfig. Time	JP1/A	JP1B
í78μs	86μs	840μs	Closed	Closed
285μs	316μs	1680μs	Open	Closed
563μs	624μs	1680μs	Closed	Open
1190μs	1237μs	1680μs	Open	Open

BOOT ROM	
Setting	JP1C
íDisabled	Open
Enabled	Closed

TOPOLOGY CONFIGURATION	
Setting	JP9
íStar	Closed
Bus	Open

I/O BASE ADDRESS			
Address	SW1/1	SW1/2	SW1/3
260h	On	On	On
290h	On	On	Off
2E0h	On	Off	On
2F0h	On	Off	Off
300h	Off	On	On
350h	Off	On	Off
380h	Off	Off	On
3E0h	Off	Off	Off

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Red	On/Blinking	Data is being transmitted or receiving
LED1	Red	Off	Data is not being transmitted or receiving
LED2	Green	On	Card is configured correctly
LED2	Green	Blinking	Card is reconfigurin g
LED2	Green	Off	Card is defective or not configured correctly