# GATEWAY COMMUNICATIONS, INC.
# G/ETHERTWIST 8 ADAPTER

| | |
|---|---|
| **NIC Type** | Ethernet |
| **Transfer Rate** | 10Mbps |
| **Data Bus** | 8-bit ISA |
| **Topology** | Linear Bus |
| | Star |
| **Wiring Type** | Shielded twisted pair |
| | Unshielded twisted pair |
| | AUI transceiver via DB-15 port |
| **Boot ROM** | Available |



| INTERRUPT REQUEST | | | | | | |
|---|---|---|---|---|---|---|
| IRQ | JP2A | JP2B | JP2C | JP2D | JP2E | JP2F |
| í3 | Open | Closed | Open | Open | Open | Open |
| 2 | Closed | Open | Open | Open | Open | Open |
| 4 | Open | Open | Closed | Open | Open | Open |
| 5 | Open | Open | Open | Closed | Open | Open |
| 6 | Open | Open | Open | Open | Closed | Open |
| 7 | Open | Open | Open | Open | Open | Closed |

| CABLE TYPE | |
|---|---|
| Type | JP4 |
| íRG-58A/U 50ohm coaxial | Pins 1 & 2 Closed |
| AUI transceiver via DB-15 port | Pins 2 & 3 Closed |

| LINK INTEGRITY TEST | |
|---|---|
| Setting | JP5 |
| íEnabled | Pins 1 & 2 Closed |
| Disabled | Pins 2 & 3 Closed |
| Note: Link Integrity is only used with 10Base-T wiring.  When using an AUI transciever, the test should be disabled. | |
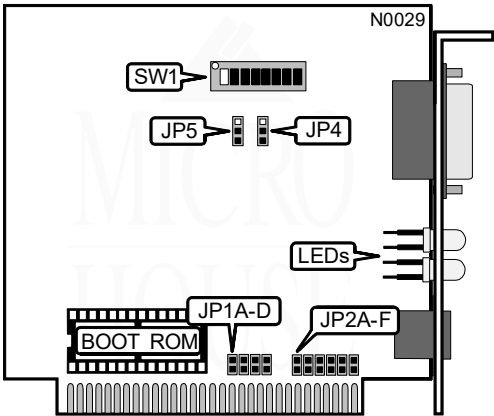
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| I/O BASE ADDRESS | | | | |
|---|---|---|---|---|
| Address | SW1/1 | SW1/2 | SW1/3 | SW1/4 |
| í2A0-2BF | On | Off | On | On |
| 280-29F | On | On | On | On |
| 2C0-2DF | On | On | Off | On |
| 2E0-2FF | On | Off | Off | On |
| 300-31F | On | On | On | Off |
| 320-33F | On | Off | On | Off |
| 340-35F | On | On | Off | Off |
| 360-37F | On | Off | Off | Off |

| BOOT ROM ADDRESS | | |
|---|---|---|
| Address | SW1/5 | SW1/6 |
| íCC000-CFFFFh | Off | Off |
| C0000-C3FFFh | On | On |
| C4000-C7FFFh | Off | On |
| C8000-CBFFFh | On | Off |

**1  2**

**3  4**
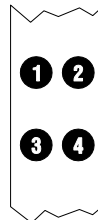
| DIAGNOSTIC LED(S) | | |
|---|---|---|
| LED | Status | Condition |
| 1 | On | Card is sending random data (Jabbering) |
| 1 | Off | Card is configured correctly and is operating normally |
| 2 | On | Network connection is good[1] |
| 2 | Off | Card is not connected to network[1] |
| 2 | On | Link test is disabled[2] |
| 2 | Blinking | Disabled the link test signal[3] |
| 3 | Blinking | Data is being transmitted |
| 3 | Off | Data is not being transmitted |
| 4 | Blinking | Data is being received |
| 4 | Off | Data is not being received |
| Note [1] :  Used when the Link Test signal is enabled (JP5) | | |
| Note [2] :  LED remains on when the Link Test signal is disabled (JP5) | | |
| Note [3] :  When the Link Test signal is enabled and the card is connected to a hub that does not use the Link Test signal, the user should disable the Link Test signal. | | |

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| FACTORY CONFIGURED SETTINGS - DO NOT ALTER | |
|---|---|
| Jumper | Setting |
| JP1 | Unknown (Used for Channel configuration) |
| Note: Link Integrity is only used with 10Base-T wiring.  When using an AUI transciever, the test should be disabled. | |

| MISCELLANEOUS NOTES |
|---|
| Different drivers must be used for the National & the Fujitsu chipset cards, they are not interchangeable. Fujitsu drivers may be used for either 8 or 16 bit Fujitsu cards, while the National chipset cards require a different driver for an 8 or 16 bit card. |