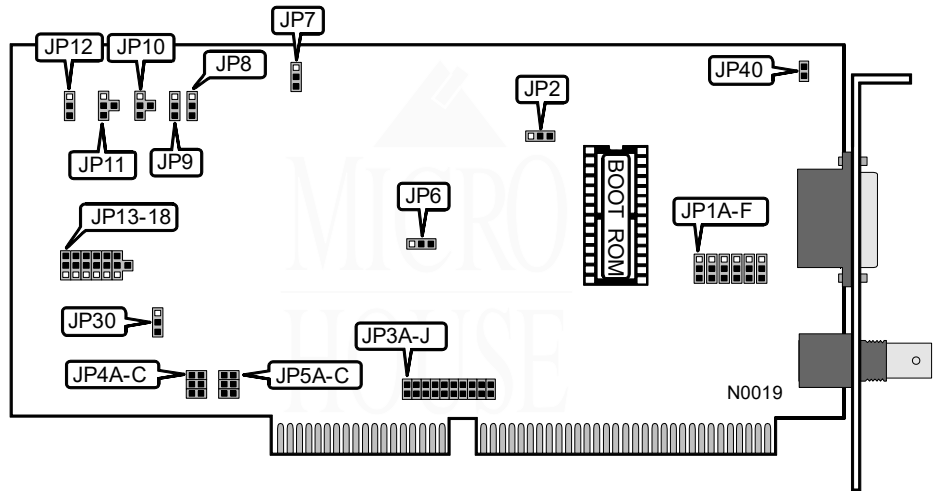


THE NETWORK INTERFACE CARD TECHNICAL GUIDE

COGENT DATA TECHNOLOGIES, INC.

E/M A S T E R I - A T

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Linear Bus
Wiring Type	RG58A/U 50ohm coaxial
	AUI transceiver via DB-15 port
Boot ROM	Available



CABLE TYPE SELECTION		
Type	JP1A-F	JP2
RG58A/U 50ohm coaxial	Pins 2 & 3 closed	Pins 2 & 3 closed
AUI transceiver via DB-15 port	Pins 1 & 2 closed	Pins 1 & 2 closed

INTERRUPT REQUEST SELECTION										
IRQ	JP3A	JP3B	JP3C	JP3D	JP3E	JP3F	JP3G	JP3H	JP3I	JP3J
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
2/9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
12	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open
14	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
15	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open

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E/MASTER I-AT

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DMA CHANNEL SELECTION						
Channel	JP4A	JP4B	JP4C	JP5A	JP5B	JP5C
iDMA5	Closed	Open	Open	Closed	Open	Open
DMA6	Open	Closed	Open	Open	Closed	Open
DMA7	Open	Open	Closed	Open	Open	Closed
Note: Each E/MASTER II in the server must have a unique DMA setting.						

BOOT ROM SIZE SELECTION	
Size	JP6
i8KB	Pins 2 & 3 closed
16KB	Pins 1 & 2 closed

CLOCK SELECTION	
Select	JP7
iClock on E/MASTER adapter (10MHz)	Pins 1 & 2 closed
Clock on the host bus	Pins 2 & 3 closed

BOOT ROM ADDRESS SELECTION						
Address	JP13	JP14	JP15	JP16	JP17	JP18
iD8000h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
C0000h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
C8000h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
D0000h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
E0000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
E8000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
Note: Pins designated should be in the closed position.						

SA 19-17 LINE CONFIGURATION	
Setting	JP30
iSA 19-17 lines enabled	Pins 1 & 2 closed
SA 19-17 lines disabled	Pins 2 & 3 closed

AUI GROUND CONFIGURATION	
Setting	JP40
iUnused shield pins on AUI connector grounded	Closed
Unused shield pins on AUI connector not grounded	Open

FACTORY CONFIGURED SETTINGS - DO NOT ALTER	
Jumper	Setting
JP8	Pins 1 & 2 closed
JP9	Pins 1 & 2 closed
JP10	Pins 2 & 3 closed
JP11	Pins 2 & 3 closed
JP12	Pins 1 & 2 closed