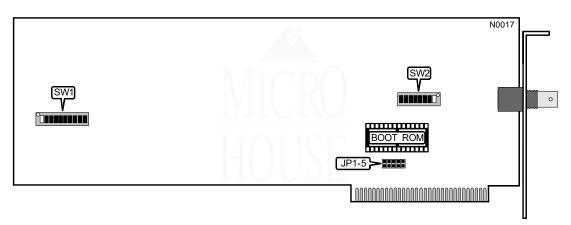
## STANDARD MICROSYSTEMS CORPORATION ARCnet PC100

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 8-bit ISA
Topology Star

Wiring Type RG-62A/U 93ohm coaxial

Boot ROM Available



NODE ADDRESS SELECTION								
Node	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
0	-	-	ı	1	-	-	-	-
1	Off	On						
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off						
255	Off							

Note: Node address 0 is used for messaging between nodes and must not be used.

A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64, 8=128. Turn off the switches and add the values of the off switches to obtain the correct node ad Iress. (On=0, Off=1)

Continued on next page . . .

## STANDARD MICROSYSTEMS CORPORATION ARCNET PC100

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INTERRUPT REQUEST SELECTION						
IRQ	JP1	JP2	JP3	JP4	JP5	
2	Open	Open	Open	Open	Closed	
3	Open	Open	Open	Closed	Open	
4	Open	Open	Closed	Open	Open	
5	Open	Closed	Open	Open	Open	
7	Closed	Open	Open	Open	Open	

I/O BASE ADDRESS SELECTION							
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	
2E0h	Off	On	Off	Off	Off	On	
2F0h	Off	On	Off	Off	Off	Off	
300h	Off	Off	On	On	On	On	

BUFFER ADDRESS SELECTION							
Address	SW1/7	SW1/8	SW1/9	SW1/10			
D000h	Off	Off	On	Off			
C000h	Off	Off	On	On			
E000h	Off	Off	Off	On			