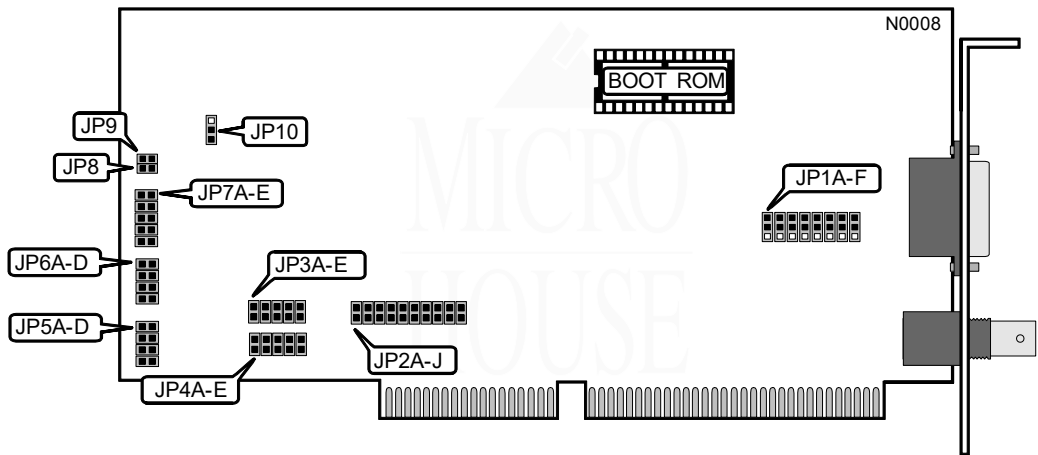


THE NETWORK INTERFACE CARD TECHNICAL GUIDE

COGENT DATA TECHNOLOGIES, INC.

E/MASTER II-A T

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Linear Bus
Wiring Type	RG58A/U 50ohm coaxial
	AUI transceiver via DB-15 port
Boot ROM	Available



CABLE TYPE SELECTION	
Type	JP1A - JP1H
RG58A/U 50ohm coaxial	Pins 1 & 2 closed
AUI transceiver via DB-15 port	Pins 2 & 3 closed

INTERRUPT REQUEST SELECTION										
IRQ	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E	JP2/F	JP2/G	JP2/H	JP2/I	JP2/J
2/9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
12	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open
14	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
15	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open

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E/MASTER II-AT

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DMA CHANNEL SELECTION										
Channel	JP3A	JP3B	JP3C	JP3D	JP3E	JP4F	JP4G	JP4H	JP4I	JP4J
DMA0	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Closed
DMA3	Open	Open	Open	Closed	Open	Open	Open	Open	Closed	Open
iDMA5	Open	Open	Closed	Open	Open	Open	Open	Closed	Open	Open
DMA6	Open	Closed	Open	Open	Open	Open	Closed	Open	Open	Open
DMA7	Closed	Open	Open	Open	Open	Closed	Open	Open	Open	Open
Note: Each E/MASTER II in the server must have a unique DMA setting.										

I/O BASE ADDRESS SELECTION				
Address	JP5A	JP5B	JP5C	JP5D
i300h	Closed	Open	Open	Open
320h	Open	Closed	Open	Open
340h	Open	Open	Closed	Open
360h	Open	Open	Open	Closed

CARD ID SELECTION				
Setting	JP6A	JP6B	JP6C	JP6D
iA	Closed	Open	Open	Open
B	Open	Closed	Open	Open
C	Open	Open	Closed	Open
D	Open	Open	Open	Closed
Note: Select a different card ID for each E/MASTER II installed. Typically, the same I/O Base address is selected for all subnets in a network server, with a different card ID selected for each subnet.				

BOOT ROM ADDRESS SELECTION					
Address	JP7A	JP7B	JP7C	JP7D	JP7e
iDisabled	Open	Open	Open	Open	Closed
C0000h	Closed	Open	Open	Open	Open
D0000h	Open	Closed	Open	Open	Open
D8000h	Open	Open	Closed	Open	Open
DC000h	Open	Open	Open	Closed	Open
Note: If using a 16-bit VGA card and a boot ROM, you may need to put the VGA card into an 8-bit slot or set the VGA card to 8-bit mode.					

BOOT ROM SIZE SELECTION		
Size	JP8	JP9
i16KB	Closed	Open
8KB	Open	Closed

SA 19-17 LINE CONFIGURATION	
Setting	JP10
iSA 19-17 lines enabled	Pins 1 & 2 closed
SA 19-17 lines disabled	Pins 2 & 3 closed