KOUWELL ELECTRONIC CORPORATION KW-557TB

Card Type Hard Drives supported Floppy drives supported

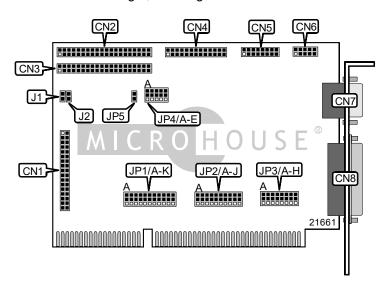
Data Bus
Card Size

Hard drive controller card Four EIDE drives

Two 360KB, 720KB, 1.2MB, or 1.44MB drives

16-bit ISA

Half-length, full-height card



CONNECTIONS					
Function Location Function					
34-pin cable connector - floppy drive	CN1	10-pin serial port 2 - internal	CN6		
40-pin primary IDE(AT) connector	CN2	9-pin serial port 1 - external	CN7		
40-pin secondary IDE(AT) connector	CN3	25-pin parallel port A - external	CN8		
Parallel port B internal	CN4	2-pin connector - drive active LED IDE1	J1		
16-pin game port	CN5	2-pin connector - drive active LED IDE2	J2		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í IDE(1) interface enabled	JP4/A	Pins 1 & 2 closed
IDE(1) interface disabled	JP4/A	Pins 2 & 3 closed
í IDE(2) interface enabled	JP4/B	Pins 1 & 2 closed
IDE(2) interface disabled	JP4/B	Pins 2 & 3 closed
í Floppy drive interface enabled	JP4/C	Pins 1 & 2 closed
Floppy drive interface disabled	JP4/C	Pins 2 & 3 closed
í Floppy drive order is normal (A:, B:)	JP4/D	Pins 1 & 2 closed
Floppy drive order is reversed (B:, A:)	JP4/D	Pins 2 & 3 closed
í Floppy drive primary	JP4/E	Pins 1 & 2 closed
Floppy drive secondary	JP4/E	Pins 2 & 3 closed
í Game port enabled	JP5	Closed
Game port disabled	JP5	Open

PRINTER A DMA CHANNEL SELECTION								
Channel	Channel JP2/G JP2/H JP2/I JP2/J							
1	Pins 1 & 2 closed	Open	Pins 2 & 3 closed	Open				
3	Open	Pins 2 & 3 closed						

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PRINTER B DMA CHANNEL SELECTION								
Channel	Channel JP2/G JP2/H JP2/I JP2/J							
1	Pins 2 & 3 closed Open Pins 2 & 3 closed Open							
3	Open	Pins 2 & 3 closed	Open	Pins 2 & 3 closed				

PARALLEL PORT A ADDRESS SELECTION					
Setting	JP3/A	JP3/B			
LPT1 (378h)	Pins 2 & 3 closed	Pins 1 & 2 closed			
LPT2 (278h)	Pins 2 & 3 closed	Pins 2 & 3 closed			
LPT3 (3BCh)	Pins 1 & 2 closed	Pins 1 & 2 closed			
Disabled	Pins 1 & 2 closed	Pins 2 & 3 closed			

PARALLEL PORT B ADDRESS SELECTION						
Setting	JP3/C	JP3/D				
LPT1 (378h)	Pins 2 & 3 closed	Pins 1 & 2 closed				
LPT2 (278h)	Pins 1 & 2 closed	Pins 2 & 3 closed				
LPT3 (3BCh)	Pins 2 & 3 closed	Pins 2 & 3 closed				
Disabled	Pins 1 & 2 closed	Pins 2 & 3 closed				

SERIAL PORT 1 ADDRESS SELECTION						
Setting JP1/A JP1/B						
COM1 (3F8h)	Pins 1 & 2 closed	Pins 1 & 2 closed				
COM3 (3E8h)	Pins 2 & 3 closed	Pins 2 & 3 closed				
COM4 (2E8h)	Pins 1 & 2 closed	Pins 2 & 3 closed				
Disabled	Pins 2 & 3 closed	Pins 1 & 2 closed				

SERIAL PORT 2 ADDRESS SELECTION					
Setting	JP1/C	JP1/D			
COM2 (2F8h)	Pins 1 & 2 closed	Pins 1 & 2 closed			
COM3 (3E8h)	Pins 1 & 2 closed	Pins 2 & 3 closed			
COM4 (2E8h)	Pins 2 & 3 closed	Pins 2 & 3 closed			
Disabled	Pins 2 & 3 closed	Pins 1 & 2 closed			

	SERIAL PORT 1 INTERRUPT SELECTION								
IRQ	JP1/E	JP1/F	JP1/G	JP1/H	JP1/I	JP1/J	JP1/K		
IRQ3	1 & 2	Open	Open	Open	Open	Open	Open		
IRQ4	Open	1 & 2	Open	Open	Open	Open	Open		
IRQ5	Open	Open	1 & 2	Open	Open	Open	Open		
IRQ9	Open	Open	Open	1 & 2	Open	Open	Open		
IRQ10	Open	Open	Open	Open	1 & 2	Open	Open		
IRQ11	Open	Open	Open	Open	Open	1 & 2	Open		
IRQ12	Open	Open	Open	Open	Open	Open	1 & 2		
Note: Pir	ns designated	should be in th	ne closed posi	tion.					

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	SERIAL PORT 2 INTERRUPT SELECTION							
IRQ	JP1/E	JP1/F	JP1/G	JP1/H	JP1/I	JP1/J	JP1/K	
IRQ3	2 & 3	Open	Open	Open	Open	Open	Open	
IRQ4	Open	2 & 3	Open	Open	Open	Open	Open	
IRQ5	Open	Open	2 & 3	Open	Open	Open	Open	
IRQ9	Open	Open	Open	2 & 3	Open	Open	Open	
IRQ10	Open	Open	Open	Open	2 & 3	Open	Open	
IRQ11	Open	Open	Open	Open	Open	2 & 3	Open	
IRQ12	Open	Open	Open	Open	Open	Open	2 & 3	
Note: Pir	ns designate s	hould be in the	e closed position	on.				

	PARALLEL PORT A INTERRUPT SELECTION										
IRQ	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E	JP2/F					
IRQ5	1 & 2	Open	Open	Open	Open	Open					
IRQ7	Open	1 & 2	Open	Open	Open	Open					
IRQ9	Open	Open	1 & 2	Open	Open	Open					
IRQ10	Open	Open	Open	1 & 2	Open	Open					
IRQ11	Open	Open	Open	Open	1 & 2	Open					
IRQ12	IRQ12 Open Open Open Open 1 & 2										
Note: Pin	s designated sho	ould be in the clo	sed position.	Note: Pins designated should be in the closed position.							

	PARALLEL PORT B INTERRUPT SELECTION							
IRQ	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E	JP2/F		
IRQ5	2 & 3	Open	Open	Open	Open	Open		
IRQ7	Open		Open	Open	Open	Open		
IRQ9	Open	Open	2 & 3	Open	Open	Open		
IRQ10	Open	Open	Open	2 & 3	Open	Open		
IRQ11	Open	Open	Open	Open	2 & 3	Open		
IRQ12	Open	Open	Open	Open	Open	2 & 3		
Note: Pins	Note: Pins designated should be in the closed position.							

PARALLEL PORT A MODE SELECTION			
Setting	JP3/E	JP3/F	
SPP	Pins 1 & 2 closed	Pins 1 & 2 closed	
ECP	Pins 1 & 2 closed	Pins 2 & 3 closed	
EPP	Pins 2 & 3 closed	Pins 1 & 2 closed	
EXT2FDD	Pins 2 & 3 closed	Pins 2 & 3 closed	

PARALLEL PORT B MODE SELECTION			
Setting	JP3/G	JP3/H	
SPP	Pins 1 & 2 closed	Pins 2 & 3 closed	
ECP	Pins 1 & 2 closed	Pins 1 & 2 closed	
EPP	Pins 2 & 3 closed	Pins 1 & 2 closed	
EXT2FDD	Pins 2 & 3 closed	Pins 2 & 3 closed	