DTK COMPUTER, INC.
PTI-227W (VER. 3)

Data Bus	16-bit ISA
Card Size	Half-length, half-height card
Hard Drives supported	Two IDE (AT) drives
Floppy drives supported	Two 360KB, 720KB, 1.2MB, or 1.44MB drives
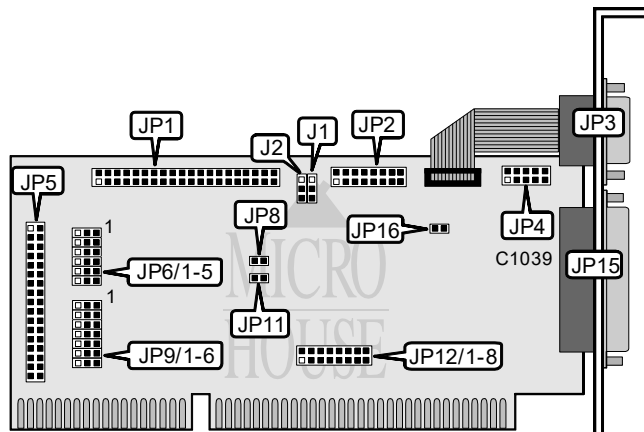
CONNECTIONS			
Function	Location	Function	Location
40-pin IDE(AT) connector	JP1	34-pin cable connector - floppy drive	JP5
16-pin game port	JP2	2-pin connector - drive active LED	JP8
9-pin serial port 1 - external	JP3	25-pin parallel port - external	JP15
10-pin serial port 2 - internal	JP4		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í IDEIOR path ATBUS IOR select	J1	Pins 2 & 3 closed
IDEIOR path W83758P chipset (IOROP) select	J1	Pins 1 & 2 closed
í IDEIOW path ATBUS IOW select	J2	Pins 2 & 3 closed
IDEIOW path W83758P chipset (IOWOP) select	J2	Pins 1 & 2 closed
í Floppy drive order is normal (A:, B:)	JP6/1	Pins 1 & 2 closed
Floppy drive order is reversed (B:, A:)	JP6/1	Pins 2 & 3 closed
í Floppy drive I/O address is 3F0-3F7h	JP6/2	Pins 2 & 3 closed
Floppy drive I/O address is 370-377h	JP6/2	Pins 1 & 2 closed
í IDE(AT) interface enabled	JP6/3	Pins 1 & 2 closed
IDE(AT) interface disabled	JP6/3	Pins 2 & 3 closed
í IDE I/O address is 3F6, 3F7, 1F0-1F7h	JP6/4	Pins 2 & 3 closed
IDE I/O address is 376, 377, 170-177h	JP6/4	Pins 1 & 2 closed
í Floppy drive enabled	JP6/5	Pins 1 & 2 closed
Floppy drive disabled	JP6/5	Pins 2 & 3 closed
Parallel port is bidirectional	JP11	Open
Parallel port is output only	JP11	Closed
í Game port enabled	JP16	Closed
Game port disabled	JP16	Open

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PARALLEL PORT ADDRESS SELECTION		
Setting	JP9/5	JP9/6
í LPT1 (378h)	Pins 2 & 3 closed	Pins 1 & 2 closed
LPT2 (278h)	Pins 1 & 2 closed	Pins 2 & 3 closed
LPT3 (3BCh)	Pins 1 & 2 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

SERIAL PORT 1 ADDRESS SELECTION		
Setting	JP9/1	JP9/2
í COM1 (3F8h)	Pins 1 & 2 closed	Pins 2 & 3 closed
COM3 (3E8h)	Pins 2 & 3 closed	Pins 1 & 2 closed
COM4 (2E8h)	Pins 1 & 2 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

SERIAL PORT 2 ADDRESS SELECTION		
Setting	JP9/3	JP9/4
í COM2 (3F8h)	Pins 1 & 2 closed	Pins 2 & 3 closed
COM3 (3E8h)	Pins 1 & 2 closed	Pins 1 & 2 closed
COM4 (2E8h)	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT SELECTION			
IRQ	JP12/1	JP12/2	JP12/3
IRQ3	Open	Closed	Open
í IRQ4	Closed	Open	Open
IRQ5	Open	Open	Closed

SERIAL PORT 2 INTERRUPT SELECTION			
IRQ	JP12/4	JP12/5	JP12/6
í IRQ3	Closed	Open	Open
IRQ4	Open	Closed	Open
IRQ5	Open	Open	Closed

PARALLEL PORT INTERRUPT SELECTION		
IRQ	JP12/7	JP12/8
IRQ5	Open	Closed
í IRQ7	Closed	Open

MISCELLANEOUS TECHNICAL NOTES
In jumpers JP6/1-5 and JP9/1-6, pin positions 1 &2 correspond to "low" board positions. Positions 2 & 3 correspond to "high" board positions.