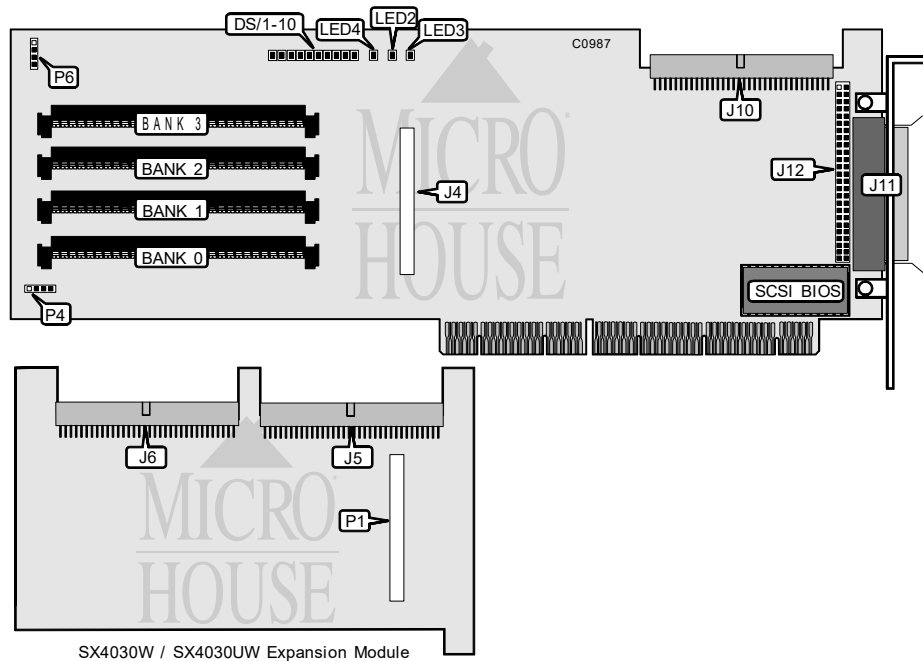


DISTRIBUTED PROCESSING TECHNOLOGY PM3332W, PM3332UW

Q3/96

Data bus:	32-bit EISA
Size:	Three-quarter length, full-height card
Hard drive supported:	Up to fifteen Wide SCSI-2 devices (PM3332W) Up to fifteen Ultra-wide SCSI devices (PM3332UW) Up to 45 Wide SCSI-2 or Ultra-wide SCSI devices with expansion board upgrades
Floppy drives supported:	None



Continued on next page. . .

DISTRIBUTED PROCESSING TECHNOLOGY

PM3332W, PM3332UW

...continued from previous page

ADAPTER CONNECTIONS	
Function	Location
3-pin connector - remote alarm	P4
4-pin connector - drive active LED	P6
Proprietary connector to SX4030expansion module	J4
68-pin SCSI bus 0 connector - internal	J10
68-pin SCSI bus 0 connector - external	J11
50-pin SCSI bus 0 connector - internal	J12
ECC error LED - red	LED2
Parity error LED - red	LED3
ECC enabled LED - green	LED4
LED - adapter busy	DS/1
LED - computer bus transfer to adapter	DS/2
LED - computer bus transfer from adapter	DS/3
LED - cache hit	DS/4
LED - disk read/ahead active	DS/5
LED - disk read	DS/6
LED - disk write	DS/7
LED - adapter reset	DS/8
LED - interrupt to computer pending	DS/9
LED - DRQ to computer asserted	DS/10

SX4030W / SX4030UW EXPANSION MODULE CONNECTION	
Function	Location
68-pin SCSI bus 1 connector - internal	J5
68-pin SCSI bus 2 connector - internal (see note)	J6
Proprietary connector from SX4030 module to adapter	P1
Note: There are four versions of the SX4030 expansion module: SX4030W/1, SX4030UW/1, SX4030W/2 and SX4030UW/2. The first two modules have a single SCSI connector (J5). This adds one bus, supporting fifteen additional devices. Expansion modules SX4030W/2 and SX4030UW/2 have two SCSI connectors (J5 and J6), supporting an 30 additional devices.	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	None	None	None
1MB	None	(1) 256K x 36	None	None
1MB	None	None	(1) 256K x 36	None
1MB	None	None	None	(1) 256K x 36
2MB	(1) 256K x 36	(1) 256K x 36	None	None

Continued on next page. . .

DISTRIBUTED PROCESSING TECHNOLOGY

PM3332W, PM3332UW

...continued from previous page

DRAM CONFIGURATION (CONTINUED)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	None	(1) 256K x 36	(1) 256K x 36	None
2MB	None	None	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
3MB	None	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	None	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	(1) 256K x 36	None	(1) 256K x 36
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	(1) 1M x 36	None	None	None
4MB	None	(1) 1M x 36	None	None
4MB	None	None	(1) 1M x 36	None
4MB	None	None	None	(1) 1M x 36
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	None	(1) 1M x 36	(1) 1M x 36	None
8MB	None	None	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	None	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	None	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	None	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None	None	None
16MB	None	(1) 4M x 36	None	None
16MB	None	None	(1) 4M x 36	None
16MB	None	None	None	(1) 4M x 36
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	None	(1) 4M x 36	(1) 4M x 36	None
32MB	None	None	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	None	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	None	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	None	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
Notes: Although not shown above, 1MB, 4MB and 16MB SIMMs can be combined and DPT ECC SIMMs can replace parity SIMMs of 4MB or 16MB. Single sided SIMMs are preferred, but low-profile double sided 2MB and 8MB increments are supported.				