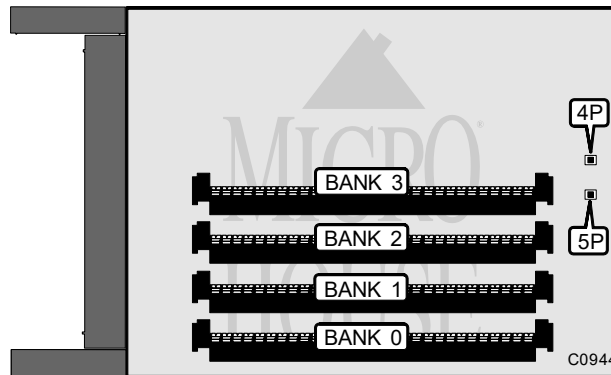
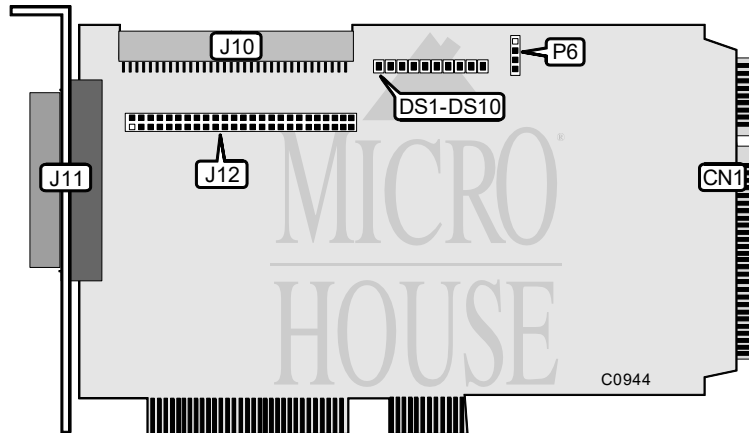


DISTRIBUTED PROCESSING TECHNOLOGY

PM2044UW/PM2044W/PM2144UW/PM2144W PM2044UW-MAC/PM2044W-MAC/PM2144UW-MAC/PM2144W-MAC

Q2/96

Data bus:	32-bit PCI
Size:	Half length, full-height card
Hard drive supported:	Fifteen SCSI-Wide, or eight SCSI-Wide with seven SCSI-2 Narrow SCSI-3 and Fast SCSI devices also supported
Floppy drives supported:	Nones



RC4040 - Optional RAID & Caching Module

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DISTRIBUTED PROCESSING TECHNOLOGY

PM2044UW/PM2044W/PM2144UW/PM2144W PM2044UW- MAC/PM2044W-MAC/PM2144UW-MAC/PM2144W-MAC

...continued from previous page

CONNECTIONS	
Function	Location
68-pin SCSI connector - internal	J10
68-pin SCSI connector - external	J11
50-pin SCSI connector internal	J12
RC4040 connector	CN1
LED - ECC enabled (green)	P4
LED - memory fault (red)	P5
4-pin connector - drive active LED	P6
LED - adapter busy	DS1
LED - computer bus transfer to adapter	DS2
LED - computer bus transfer from adapter	DS3
LED - cache hit	DS4
LED - disk read/ahead active	DS5
LED - disk read	DS6
LED - disk write	DS7
LED - adapter reset	DS8
LED - interrupt to computer pending	DS9
LED - DRQ to computer asserted	DS10
Note: Location of floppy drive interface connector P8 is unidentified.	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	None	None	None
1MB	None	(1) 256K x 36	None	None
1MB	None	None	(1) 256K x 36	None
1MB	None	None	None	(1) 256K x 36
2MB	(1) 256K x 36	(1) 256K x 36	None	None
2MB	None	(1) 256K x 36	(1) 256K x 36	None
2MB	None	None	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
3MB	None	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	None	(1) 256K x 36	(1) 256K x 36
3MB	(1) 256K x 36	(1) 256K x 36	None	(1) 256K x 36
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36

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DISTRIBUTED PROCESSING TECHNOLOGY

PM2044UW/PM2044W/PM2144UW/PM2144W PM2044UW- MAC/PM2044W-MAC/PM2144UW-MAC/PM2144W-MAC

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DRAM CONFIGURATION CONTINUED				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	None	None	None
4MB	None	(1) 1M x 36	None	None
4MB	None	None	(1) 1M x 36	None
4MB	None	None	None	(1) 1M x 36
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	None	(1) 1M x 36	(1) 1M x 36	None
8MB	None	None	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	None	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	None	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	None	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None	None	None
16MB	None	(1) 4M x 36	None	None
16MB	None	None	(1) 4M x 36	None
16MB	None	None	None	(1) 4M x 36
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	None	(1) 4M x 36	(1) 4M x 36	None
32MB	None	None	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	None	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	None	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	None	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
Notes: Although not shown above, 1MB, 4MB and 16MB SIMMs can be combined and DPT ECC SIMMs can replace parity SIMMs of 4MB or 16MB. Single sided SIMMs are preferred, but low-profile double sided 2M and 8MB increments are supported.				