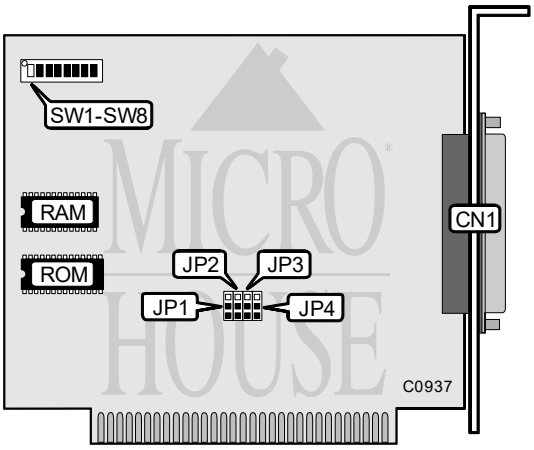
# IOMEGA CORPORATION
# PC2, PC2B

Q2/96

| | |
|---|---|
| Data bus: | 8-bit ISA |
| Size: | Half-length, full-height card |
| Hard drive supported: | None |
| Floppy drives supported: | Two Bernoulli Box drives |




| CONNECTIONS | |
|---|---|
| Function | Location |
| 34-pin Bernoulli Box connector - external drive | CN1 |

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Location | Setting |
| í DMA channel 3 select | SW3 | off |
| DMA channel 1 select | SW3 | on |
| í DMA enabled | SW4 | off |
| PIO (Programmed Input/Ouput) enabled | SW4 | on |
| í IRQ5 select | SW6 | off |
| IRQ7 select | SW6 | on |
| í Factory configured - do not alter | SW7 | off |
| í Two 10MB Bernoulli drives in system | SW8 | off |
| One 10MB Bernoulli drive in system | SW8 | on |

*Continued on next page. . .*

# IOMEGA CORPORATION
# PC2B, PC2

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| PORT ADDRESS SELECTION | | | |
|---|---|---|---|
| Address | SW1 | SW2 | SW5 |
| í 340h | on | on | off |
| 348h | on | on | on |
| 350h | off | on | off |
| 358h | off | on | on |
| 360h | on | off | off |
| 368h | on | off | on |
| 370h | off | off | off |
| Disabled | off | off | off |

| ROM ADDRESS SELECTION (PC2B only) | | | | |
|---|---|---|---|---|
| Address | JP1 | JP2 | JP3 | JP4 |
| C800:0000h - C800:1FFFh | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| CA00:0000h - CA00:1FFFh | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 |
| í CE00:0000h - CE00-1FFFh | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| D000:0000h - D000:1FFFh | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| D200:0000h - D200:1FFFh | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 |
| D400:0000h - D400:1FFFh | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| D600:0000h - D600:1FFFh | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| D800:0000h - D800:1FFFh | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| DA00:0000h - DA00:1FFFh | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 |
| DC00:0000h - DC00:1FFFh | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| DE00:0000h - DE00:1FFFh | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 |
| E000:0000h - E000:1FFFh | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| E200:0000h - E200:1FFFh | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |
| E400:0000h - E400:1FFFh | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| RAM/ROM disabled | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| Note: Designated pins should be in the closed position. | | | | |

| MISCELLANEOUS TECHNICAL NOTES |
|---|
| Model PC2 is the same as PC2B with the exception of jumper shunts, and RAM and ROM chips. PC2 can be upgraded to PC2B by ordering an upgrade kit that includes shunts and chips. |