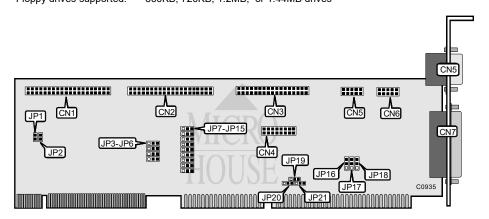
Q2/96

UNITED MICROELECTRONICS CORPORATION FSIOL-UC2.1C

Data bus: 32-bit, VL-Bus

Size: Three quarter-length, half-height card
Hard drive supported: Two IDE (AT) interface drives
Floppy drives supported: 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS		
Function	Location	
40-pin primary IDE(AT) interface	CN1	
40-pin secondary IDE(AT) interface	CN2	
34-pin control cable connector - floppy drive	CN3	
16-pin Game port	CN4	
9-pin serial port 1 - external, 10-pin serial port 1 - internal	CN5	
10-pin serial port 2 - internal	CN6	
25-pin parallel port	CN7	
2-pin drive active LED for primary IDE	JP1	
2-pin drive active LED for secondary IDE	JP2	

CPU CLOCK CONFIGURATION			
CPU Clock	JP3	JP4	
4(50 MHz	open	open	
Belov 20 MHz or Enhanc d IDE support	open	losed	
2f 33 MHz	losed	open	
Unde 40 MHz or Non-, TA support	losed	losed	

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í Primary IDE interface enabled	JP5	pins 1 & 2 closed
Primary IDE interface disabled	JP5	pins 2 & 3 closed
í Secondary IDE interface enabled	JP6	pins 1 & 2 closed
Secondary IDE interface disabled	JP6	pins 2 & 3 closed
í Floppy drive interface enabled	JP9	pins 1 & 2 closed
Floppy drive interface disabled	JP9	pins 2 & 3 closed
í Parallel port is LPT2	JP14	pins 1 & 2 closed
Parallel port is LPT3	JP14	pins 2 & 3 closed
í Game port enabled	JP15	pins 1 & 2 closed
Game port disabled	JP15	pins 2 & 3 closed
í Factory configured - do not alter	JP16	N/A
í Factory configured - do not alter	JP17	N/A
í Factory configured - do not alter	JP18	N/A
í Factory configured - do not alter	JP19	N/A
í Factory configured - do not alter	JP20	N/A
í Factory configured - do not alter	JP21	N/A

PARALLEL PORT MODE CONFIGURATION			
MODE	JP7	JP8	
í Sta dard printer	pins & 2 closed	pins: & 3 closed	
E(P/EPP	N/A	N/A	
[sabled	pins : & 3 closed	pins: & 3 closed	

SERIAL PORT 1 CONFIGURATION			
COM	JP10	JP11	
í COM1	pins & 2 closed	pins & 2 closed	
COM3	pins & 2 closed	pins: & 3 closed	
Disabled	pins: & 3 closed	N/A	

SERIAL PORT 2 CONFIGURATION			
COM	JP12	JP13	
í COM2	pins & 2 closed	pins & 2 closed	
COM4	pins & 2 closed	pins: & 3 closed	
)isabled	pins : & 3 closed	N/A	