Q4/95

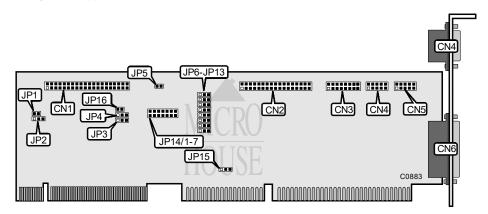
## QDI COMPUTER, INC. QD6500A VL/V2

Data bus: 32-bit VL-bus

Size: Three-quarter length, half-height card

Hard drive supported: Floppy drives supported:

Two IDE(AT) drives
Two 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS		
Function	Location	
40-pin IDE(AT) connector	CN1	
34-pin cable connector - floppy drive	CN2	
16-pin game port	CN3	
9-pin serial port 1 - external, 10-pin serial port 1 internal	CN4	
10-pin serial port 2 - internal	CN5	
25-pin parallel port - external	CN6	
2-pin connector - drive active LED	JP5	

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í VL IDE controller enabled	JP1	open
VL IDE controller disabled	JP1	closed
í Sense clock frequency from VL slot enabled	JP2	pins 2 & 3 closed
Sense clock frequency from VL slot disabled	JP2	pins 1 & 2 closed
í Chipset register address is 0BXh	JP3	pins 2 & 3 closed
Chipset register address is 03Xh	JP3	pins 1 & 2 closed
í Primary IDE port addresses are 1F0h-1F7h,3F6h,3F7h	JP4	pins 2 & 3 closed
Secondary IDE port addresses are 170h-177h,376h,377h	JP4	pins 1 & 2 closed
í Floppy drive interface enabled	JP8	pins 1 & 2 closed
Floppy drive interface disabled	JP8	pints 2 & 3 closed

Continued on next page. . .

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## QDI COMPUTER, INC. QD6500A VL/V2

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USER CONFIGURABLE SETTINGS (CONTINUED)			
Function	Location	Setting	
í Game port enabled	JP13	pins 1 & 2 closed	
Game port disabled	JP13	pins 2 & 3 closed	
í IDE IRQ 14 select	JP15	pins 1 & 2 closed	
IDE IRQ 15 select	JP15	pins 2 & 3 closed	
í Floppy address 3F7/177h enabled	JP16	closed	
Floppy address 3F7/177h disabled	JP16	open	

BIOS ADDRESS SELECTION							
Address	JP14/1	JP14/2	JP14/3	JP14/4	JP14/5	JP14/6	JP14/7
í Disabled BIOS	closed	open	open	open	open	open	open
C800H-CBFFH	open	closed	open	open	open	open	open
CC00H-CFFFH	open	open	closed	open	open	open	open
D000H-D3FFH	open	open	open	closed	open	open	open
D400H-D7FFH	open	open	open	open	closed	open	open
D800H-DBFFH	open	open	open	open	open	closed	open
DC00H-DFFFH	open	open	open	open	open	open	closed
Note: These settings apply when BIOS EPROM is installed							

	SERIAL PORT 1 ADDRESS SELECTION				
	COM	JP6	JP7		
	í COM1	pins 1 & 2 closed	pins 1 & 2 closed		
	COM3	pins 1 & 2 closed	pins 2 & 3 closed		
Disabled		pins 2 & 3 closed	pins 1 & 2 closed		
	Disabled	pins 2 & 3 closed	pins 2 & 3 closed		

SERIAL PORT 2 ADDRESS SELECTION			
СОМ	JP11	JP12	
í COM2	pins 1 & 2 closed	pins 1 & 2 closed	
COM4	pins 1 & 2 closed	pins 2 & 3 closed	
Disabled	pins 2 & 3 closed	pins 1 & 2 closed	
Disabled	pins 2 & 3 closed	pins 2 & 3 closed	

PARALLEL PORT ADDRESS SELECTION			
Address	JP9	JP10	
í 378h	pins 1 & 2 closed	pins 1 & 2 closed	
278h	pins 1 & 2 closed	pins 2 & 3 closed	
Disabled	pins 2 & 3 closed	pins 1 & 2 closed	
Disabled	pins 2 & 3 closed	pins 2 & 3 closed	