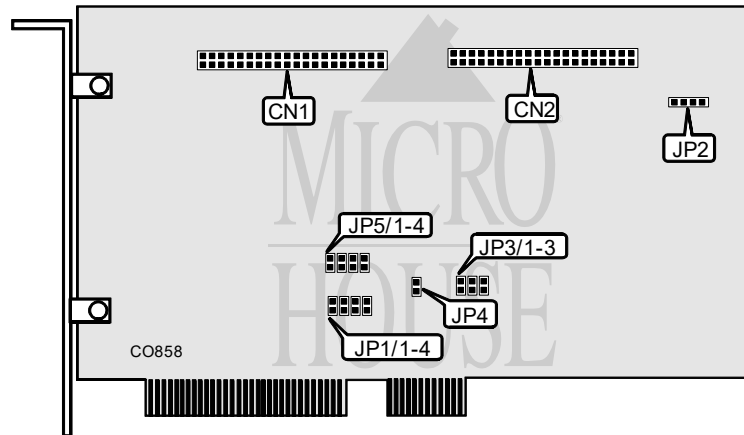


CHAINTECH COMPUTER COMPANY, LTD. IDE-PCI (TTL VERSION)

Q4/95

Data bus: 32-bit PCI
 Size: Half-length, full-height card
 Hard drive supported: Four IDE(XT) drives
 Floppy drives supported: None



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) connector	CN1
40-pin secondary IDE(AT) connector	CN2
4-pin connector - drive active LED	JP2

USER CONFIGURABLE SETTINGS			
Function	Location	Setting	
CPU clock speed $\leq 33\text{MHz}$	JP3/ 3	open	
CPU clock speed $> 33\text{MHz}$	JP3/ 3	closed	
IDE(AT) interface enabled	JP4	open	
IDE(AT) interface disabled	JP4	closed	

PCI INTERRUPT SELECTION FOR PRIMARY IDE, IRQ 14				
INT	JP1/ 1	JP1/ 2	JP1/ 3	JP1/ 4
A	closed	open	open	open
B	open	closed	open	open
C	open	open	closed	open
D	open	open	open	closed

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PCI INTERRUPT SELECTION FOR SECONDARY IDE, IRQ 15				
INT	JP5/ 1	JP5/ 2	JP5/ 3	JP5/ 4
A	closed	open	open	open
B	open	closed	open	open
C	open	open	closed	open
D	open	open	open	closed

TIMING MODE SELECTION		
Mode	JP3/ 1	JP3/ 2
0 (600ns. normal speed device)	open	open
1 (383ns. fast speed device)	closed	open
2 (240ns. turbo speed device)	open	closed