Q4/95

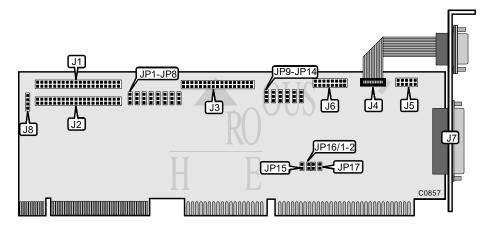
CHAINTECH COMPUTER COMPANY, LTD. ML-757

Data bus: 32-bit VL-bus

Three-quarter length, half-height card Size:

Hard drive supported:

Four IDE(AT) drives Two 360KB, 720KB, 1.2MB, or 1.44MB drives Floppy drives supported:



| CONNECTIONS | | |
|---|----------|--|
| Function | Location | |
| 40-pin primary IDE(AT) connector | J1 | |
| 40-pin secondary IDE(AT) connector | J2 | |
| 34-pin cable connector - floppy drive | J3 | |
| 10-pin serial port 1 internal, 9-pin serial port 1 - external | J4 | |
| 10-pin serial port 2 - internal | J5 | |
| 16-pin game port | J6 | |
| 25-pin parallel port - external | J7 | |
| 4-pin connector - drive active LED | Ј8 | |

| PARALLEL PORT ADDRESS SELECTION | | | |
|---------------------------------|-------------------|----------------------|--|
| LPT | JP13 | JP14 | |
| í LPT1 (378h) | pins 1 & 2 closed | pins 1 & 2 closed | |
| LPT2 (278h) | pins 2 & 3 closed | pins 2 & 3 closed | |
| LPT3 (3BCh) | pins 2 & 3 closed | ed pins 1 & 2 closed | |
| Disabled | pins 1 & 2 closed | pins 2 & 3 closed | |

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| USER CONFIGURABLE SETTINGS | | | |
|---|----------------|---------|--|
| Function | Location | Setting | |
| í IDE(AT) interface enabled | JP1/pins 1 & 2 | closed | |
| IDE(AT) interface disabled | JP1/pins 2 & 3 | closed | |
| í Primary & secondary IDE enabled | JP2/pins 1 & 2 | closed | |
| Primary IDE enabled, secondary disabled | JP2/pins 2 & 3 | closed | |
| í CPU clock is 40 or 50MHz | JP3/pins 1 & 2 | closed | |
| CPU clock <=33MHz | JP3/pins 2 & 3 | closed | |
| í Factory configured - do not alter | JP4 | N/A | |
| í Factory configured - do not alter | JP5 | N/A | |
| í Floppy drive order is normal (A:, B:) | JP6/pins 1 & 2 | closed | |
| Floppy drive order is reversed (B:, A:) | JP6/pins 2 & 3 | closed | |
| í Floppy controller primary address is 3F0h | JP7/pins 1 & 2 | closed | |
| Floppy controller secondary address is 370h | JP7/pins 2 & 3 | closed | |
| í Floppy drive interface enabled | JP8/pins 1 & 2 | closed | |
| Floppy drive interface disabled | JP8/pins 2 & 3 | closed | |
| í Parallel port is unidirectional | JP15 | closed | |
| Parallel port is bi-directional | JP15 | open | |
| í Parallel port IRQ 7 select | JP16/ 1 | closed | |
| Parallel port IRQ 5 select | JP16/ 2 | closed | |
| í Game port enabled | JP17 | closed | |
| Game port disabled | JP17 | open | |

| SERIAL PORT 1 ADDRESS SELECTION | | |
|---------------------------------|-------------------|-------------------|
| COM | JP9 | JP10 |
| í COM1 (3F8h) | pins 1 & 2 closed | pins 1 & 2 closed |
| COM3 (3E8h) | pins 2 & 3 closed | pins 2 & 3 closed |
| Disabled | pins 2 & 3 closed | pins 1 & 2 closed |

| SERIAL PORT 2 ADDRESS SELECTION | | | |
|---------------------------------|-------------------|-------------------|--|
| COM | JP11 | JP12 | |
| í COM2 (2F8h) | pins 1 & 2 closed | pins 1 & 2 closed | |
| COM4 (2E8h) | pins 2 & 3 closed | pins 2 & 3 closed | |
| Disabled | pins 2 & 3 closed | pins 1 & 2 closed | |

| MISCELLANEOUS TECHNICAL NOTES | |
|---|--|
| Pin 1 in jumper blocks is unidentified. | |