Q4/95

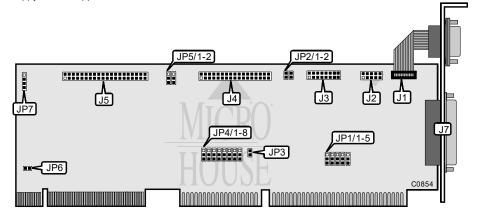
KOUWELL ELECTRONICS CORPORATION KW-559AG

Data bus: 32-bit VL-bus

Size: Three-quarter length, half-height card

Hard drive supported: Two IDE(AT) drives

Floppy drives supported: Two 360KB, 720KB, 1.2MB, or 1.44MB drives



| CONNECTIONS | | |
|---|----------|--|
| Function | Location | |
| 9-pin serial port 1 - external, 10-pin serial port 1 internal | J1 | |
| 10-pin serial port 2 - internal | J2 | |
| 16-pin game port | J3 | |
| 34-pin cable connector - floppy drive | J4 | |
| 40-pin primary IDE(AT) connector | J5 | |
| 25-pin parallel port - external | J7 | |
| 4-pin connector - drive active LED | JP7 | |

| USER CONFIGURABLE SETTINGS | | | |
|-----------------------------------|----------|-------------------|--|
| Function | Location | Setting | |
| í Parallel port is bi-directional | JP3 | open | |
| Parallel port is unidirectional | JP3 | closed | |
| í Floppy drive interface enabled | JP4/7 | pins 1 & 2 closed | |
| Floppy drive interface disabled | JP4/7 | pins 2 & 3 closed | |
| í Parallel port IRQ 7 select | JP4/8 | pins 2 & 3 closed | |
| Parallel port IRQ 5 select | JP4/8 | pins 1 & 2 closed | |
| í CPU speed is <=33MHz | JP6 | open | |
| CPU speed is >33MHz | JP6 | closed | |

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| DRIVE SPEED CONFIGURATION | | | | |
|---|-------------------|-------------------|--|--|
| Speed JP5/1 JP5/2 | | | | |
| í 0 pins 1 & 2 closed pins 1 & 2 closed | | | | |
| 1 pins 2 & 3 closed pins 1 & 2 closed | | | | |
| 2 | pins 2 & 3 closed | pins 2 & 3 closed | | |

| SERIAL PORT 1 INTERRUPT SELECTION | | | | | |
|-----------------------------------|---------------------|---------------------|---------------------|---------------------|---------------------|
| IRQ | JP1/1 pins 1 & 2 | JP1/2 pins 1 & 2 | JP1/3 pins 1 & 2 | JP1/4 pins 1 & 2 | JP1/5 pins 1 & 2 |
| í IRQ4 | open | closed | open | open | open |
| IRQ3 | closed | open | open | open | open |
| IRQ5 | open | open | closed | open | open |
| IRQ9 | open | open | open | closed | open |
| IRQ12 | open | open | open | open | closed |

| SERIAL PORT 2 INTERRUPT SELECTION | | | | | |
|-----------------------------------|---------------------|---------------------|---------------------|---------------------|---------------------|
| IRQ | JP1/1 pins 2 & 3 | JP1/2 pins 2 & 3 | JP1/3 pins 2 & 3 | JP1/4 pins 2 & 3 | JP1/5 pins 2 & 3 |
| í IRQ3 | closed | open | open | open | open |
| IRQ4 | open | closed | open | open | open |
| IRQ5 | open | open | closed | open | open |
| IRQ9 | open | open | open | closed | open |
| IRQ12 | open | open | open | open | closed |

| PARALLEL PORT ADDRESS SELECTION | | | |
|--|-------------------|-------------------|--|
| LPT JP4/5 JP4/6 | | | |
| í 378h | pins 2 & 3 closed | | |
| 278h | pins 1 & 2 closed | | |
| 3BCh pins 1 & 2 closed pins 1 & 2 closed | | | |
| Disabled | pins 2 & 3 closed | pins 2 & 3 closed | |

| SERIAL PORT 1 ADDRESS SELECTION | | | |
|---------------------------------|-------------------|-------------------|--|
| COM | JP4/1 | JP4/2 | |
| í COM1 (3F8h) | pins 1 & 2 closed | pins 2 & 3 closed | |
| COM3 (3E8h) | pins 1 & 2 closed | | |
| COM4 (2E8h) | pins 1 & 2 closed | pins 1 & 2 closed | |
| Disabled | pins 2 & 3 closed | pins 2 & 3 closed | |

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| SERIAL PORT 2 ADDRESS SELECTION | | | |
|---------------------------------|-------------------|-------------------|--|
| COM | JP4/3 | JP4/4 | |
| í COM2 (2F8h) | pins 1 & 2 closed | pins 2 & 3 closed | |
| COM3 (3E8h) | pins 1 & 2 closed | | |
| COM4 (2E8h) | pins 2 & 3 closed | pins 1 & 2 closed | |
| Disabled | pins 2 & 3 closed | pins 2 & 3 closed | |

| GAME PORT CONFIGURATION | | | | |
|-------------------------|------|------|--|--|
| Mode JP2/1 JP2/2 | | | | |
| í Enabled closed closed | | | | |
| Disabled | open | open | | |