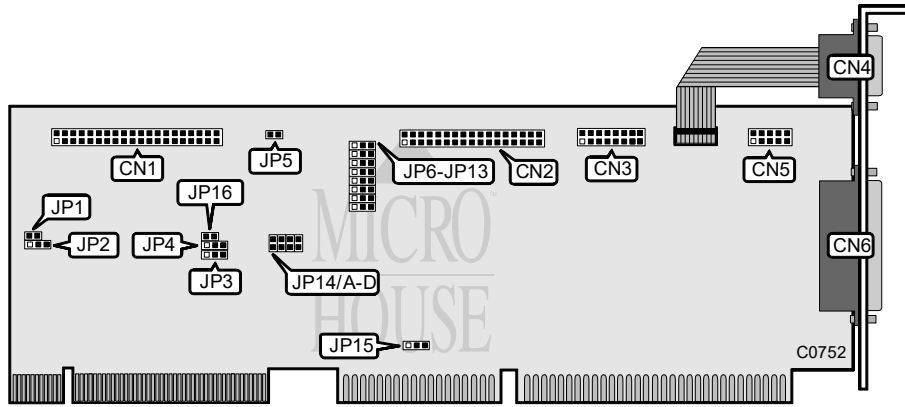


QDI COMPUTER, INC.

QD6500, QD6500A VL

Q2/95

Data bus: 32-bit, VL-bus
 Size: Three/quarter-length, half-height card
 Hard drive supported: Two IDE (AT) interface drives
 Floppy drives supported: Two 360KB, 720KB, 1.2MB or 1.44MB drives



CONNECTIONS	
Function	Location
40-pin IDE (AT) interface connector	CN1
34-pin control cabled connector - floppy drive	CN2
Game port	CN3
9-pin serial port 1 - external	CN4
10-pin serial port 2 - internal	CN5
25-pin parallel port	CN6
2-pin connector - drive active LED	JP5

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
i Factory configured - do not alter	JP1	open
i Factory configured - do not alter	JP2	pins 2 & 3 closed
i Chipset register address = 0BXh	JP3	pins 2 & 3 closed
Chipset register address = 03Xh	JP3	pins 1 & 2 closed
i IDE port address = 1F0h	JP4	pins 2 & 3 closed
IDE port address = 170h	JP4	pins 1 & 2 closed
i Floppy drive enabled	JP8	pins 1 & 2 closed
Floppy drive disabled	JP8	pins 2 & 3 closed
i Game port enabled	JP13	pins 1 & 2 closed
Game port disabled	JP13	pins 2 & 3 closed

Continued on next page . . .

QDI COMPUTER, INC.
QD6500, QD6500A VL

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í IDE IRQ = IRQ14	JP15	pins 1 & 2 closed
IDE IRQ = IRQ15	JP15	pins 2 & 3 closed
Address lines 3F7/177h enabled (74LS244 chip installed)	JP16	closed
Address lines 3F7/177h disabled	JP16	open

SERIAL PORT 1 CONFIGURATION		
COM	JP6	JP7
í COM1	pins 1 & 2 closed	pins 1 & 2 closed
COM3	pins 1 & 2 closed	pins 2 & 3 closed
Disabled	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 2 CONFIGURATION		
COM	JP11	JP12
í COM2	pins 1 & 2 closed	pins 1 & 2 closed
COM4	pins 1 & 2 closed	pins 2 & 3 closed
Disabled	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

PARALLEL PORT CONFIGURATION		
LPT/Address	JP9	JP10
í LPT1/378h	pins 1 & 2 closed	pins 1 & 2 closed
LPT2/278h	pins 1 & 2 closed	pins 2 & 3 closed
Disabled	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

BIOS ADDRESS - JP14				
Address	Jumper A	Jumper B	Jumper C	Jumper D
í Disabled	closed	open	open	open
C800h	open	closed	open	open
D000h	open	open	closed	open
D800h	open	open	open	closed