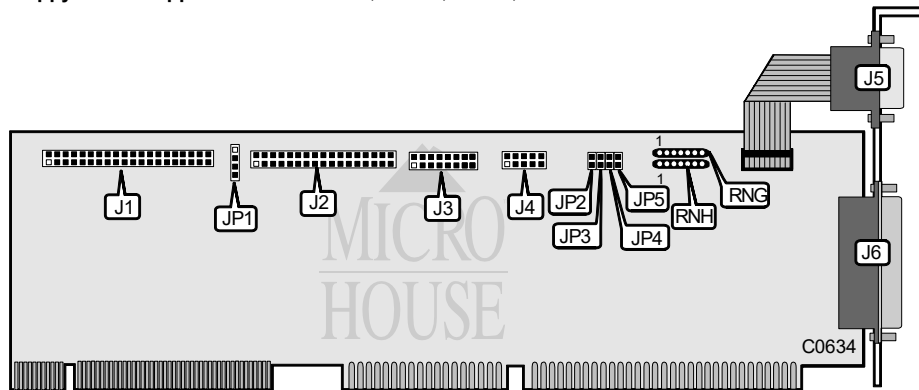


## TMC RESEARCH CORPORATION IFSPVL (VER. 2B)

Q3/95

**Data bus:** 32-bit, VL-Bus  
**Size:** Three/quarter-length, full-height card  
**Hard drive supported:** Two IDE (AT) interface drives  
**Floppy drives supported:** Two 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS	
Function	Location
40-pin IDE(AT) interface connector	J1
34-pin control cable connector - floppy drive	J2
16-pin game port - internal	J3
10-pin serial port (COM2/4) - internal	J4
9-pin serial port (COM1/3) - external	J5
25-pin parallel port (LPT1/2) - external	J6
4-pin connector - Drive active LED	JP1

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# TMC RESEARCH CORPORATION

## IFSPVL (VER. 2B)

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
IDE(AT) controller enabled	JP2	closed
IDE(AT) controller disabled	JP2	open
CPU is 386DX	JP3	open
CPU is 486 (SX, DX, DX2, Overdrive, 487SX, P24T)	JP3	closed
Floppy controller enabled	JP4	closed
Floppy controller disabled	JP4	open
Game port enabled	JP5	closed
Game port disabled	JP5	open
Parallel port (J6) is LPT1	RNG	pins 2-5 closed
Parallel port (J6) is LPT2	RNG	pins 1-4 closed
Parallel port disabled	RNG	pins 4-7 closed
Note: RNG is configured by installing a 4-pin resistor network over the corresponding pins.		

SERIAL PORTS CONFIGURATIONS		
Port 1 (J5)	Port 2 (J4)	RNH
COM1	COM2	pins 1-4 closed
COM1	disabled	pins 2-5 closed
disabled	COM2	pins 3-6 closed
disabled	disabled	pins 4-7 closed
Note: RNH is configured by installing a 4-pin resistor network over the corresponding pins.		