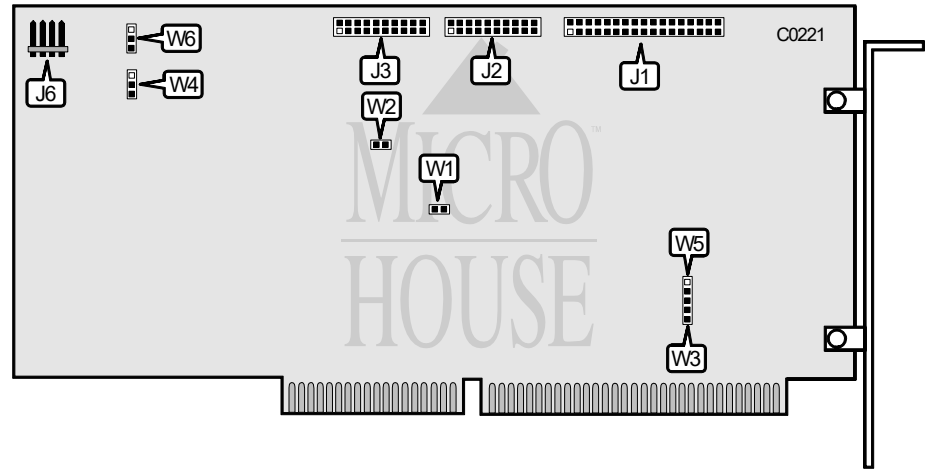


WESTERN DIGITAL CORPORATION
WD1003-RAH, WD1003-WAH
*Replaced by the WD1003V-SR1
and WD1003V-MM1 respectively*

Q2/92

Data bus:	16-bit, PC/AT
Size:	Full-length, full-height card
Hard drives supported:	WD1003-RAH: Two RLL ST506/412 drives WD1003-WAH: Two MFM ST506/412 drives
Floppy drives supported:	None



CONNECTIONS	
Function	Location
34-pin data cable connector-hard drive	J1
20-pin data cable connector-drive 0	J2
20-pin data cable connector-drive 1	J3
4-pin connector-drive active LED	J6

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WESTERN DIGITAL CORPORATION

WD1003-RAH, WD1003-WAH

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USER CONFIGURABLE SETTING - WD1003-RAH		
Function	Location	Setting
í Auto deselect enabled (PC/AT)	W1	closed
Auto deselect disabled (Compaq)	W1	open
í Hard drive port address is 1F0-1F7 & 3F0-3F7h	W2	open
Hard drive port address is 170-177 & 370-377h	W2	closed
WD11C00C-22 chip at U8 or if W5/pins 2-3 closed	W3	open
WD11C00-22 chip at U8 & W5/pins 1-2 closed	W3	closed
í Drive 0 translation disabled	W4	pins 2 & 3 closed
Drive 0 translation enabled	W4	pins 1 & 2 closed
í Write gate and drive select lines enabled only when drive is accessed	W5	pins 2 & 3 closed
Write gate and drive select lines are disabled during power-up and when +5V is < +4.15V	W5	pins 1 & 2 closed
í Drive 1 logical translation disabled	W6	pins 2 & 3 closed
Drive 1 logical translation enabled	W6	pins 1 & 2 closed

USER CONFIGURABLE SETTINGS - WD1003-WAH		
Function	Location	Setting
í Auto deselect enabled (PC/AT)	W1	closed
Auto deselect disabled (Compaq)	W1	open
í Hard drive port address is 1F0-1F7 & 3F0-3F7	W2	open
Hard drive port address is 170-177 & 370-377	W2	closed
WD11C00A-22 chip at U8 or if W5/pins 2-3 closed	W3	open
WD11C00-22 chip at U8 & W5/pins 1-2 closed	W3	closed
í Ties firmware sense bit high	W4	pins 2 & 3 closed
Supports 2 head, 612 cyl. second drive with standard system setup for 4 head, 306 cyl. drive	W4	pins 1 & 2 closed
í Write gate and drive select lines enabled only when drive is accessed	W5	pins 2 & 3 closed
Write gate and drive select lines are disabled during power-up and when +5V is < +4.15V	W5	pins 1 & 2 closed
í Ties input high, 35 μ sec step rate selected	W6	pins 2 & 3 closed
Ties input low, 16 μ sec step rate selected	W6	pins 1 & 2 closed