SILICON VALLEY COMPUTER, INC. ADP65

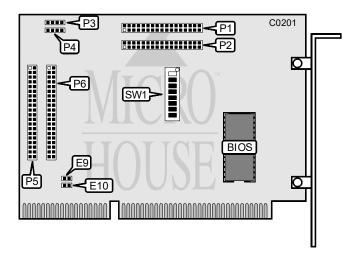
Q4/92

Data bus: 16-bit, ISA

Size:

Half-length, full-height card
Up to four IDE (AT) Interface drives or up to two IDE tape drives
Up to four 360KB, 720KB, 1.2MB, or 1.44MB Hard drives supported:

Floppy drives supported:



CONNECTIONS		
Function	Location	
34-pin control cable connector-floppy drives 0 and 1	P1	
34-pin control cable connector-floppy drives 2 and 3	P2	
4-pin connector-Drive active LED for P5	P3	
4-pin connector-Drive active LED for P6	P4	
40-pin IDE (AT) Interface connector (Drives 0 and 1)	P5	
40-pin IDE (AT) Interface connector (Drives 2 and 3)	P6	
Note: Connector P5 only supports hard drives, any tape drives must be chained from P6.		

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	USER CONFIGURABLE SETTINGS				
	Function	Location	Setting		
í Re	eserved for ROM Size, must be left as shipped	SW1/switch 7	off		
í Flo	oppy drive enabled	SW1/switch 8	on		
Flo	oppy drive disabled	SW1/switch 8	off		
í IR	Q15 enabled on CN6	E9	closed		
IR	Q15 disabled	E9	open		
í IR	Q14 enabled on CN5	E10	closed		
IR	Q14 disabled	E10	open		

PORT ADDRESS CONFIGURATION						
HD Port 1 (CN5)	HD Port 2 (CN6)	SW1/switch 4	SW1/switch 5	SW1/switch 6		
Disabled	Disabled	off	off	off		
1F0-1F7h	Disabled	off	on	off		
170-177h	Disabled	off	on	on		
1F0-1F7h	170-177h	on	on	off		

BIOS ADDRESS					
Address	SW1/switch 1	SW1/switch 2	SW1/switch 3		
Disabled	off	off	off		
í C800h / 8K	on	off	off		
CA00h / 8K	off	on	off		
C800h / 16K	on	on	off		
CC00h / 8K	on	off	on		
CE00h / 8K	off	on	on		
CC00h / 16K	on	on	on		