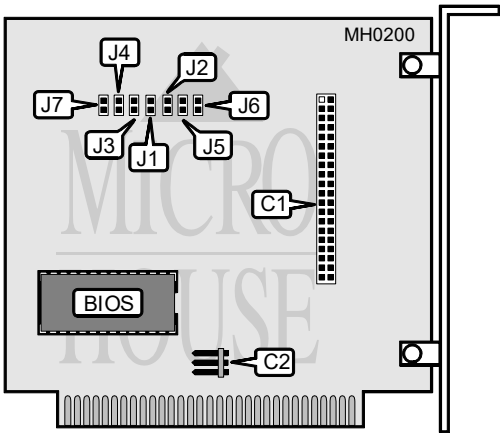


SILICON VALLEY COMPUTER, INC.
ADP50L

Q1/93

Data bus: 8-bit, ISA
Size: Half-length, full-height card
Hard drives supported: Two IDE (AT) Interface drives
Floppy drives supported: None



CONNECTIONS	
Function	Location
40-pin AT Interface (IDE) connector	C1
5-pin connector-DC power	C2

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
i Factory configured - do not alter	J4	open
i Factory configured - do not alter	J5	open
i Factory configured - do not alter	J6	open
i Uses 2764 or 27C64 ROM Chip	J7	open
Uses 27256 or 27C256 ROM Chip	J7	closed

BIOS ADDRESS			
Address	Jumper J1	Jumper J2	Jumper J3
Disabled	N/A	N/A	
i C800	open	open	closed
CA00	closed	open	closed
CC00	open	closed	closed
CE00	closed	closed	closed