SCIENTIFIC MICRO SYSTEMS
OMTI 810, OMTI 812, OMTI 820, OMTI 822
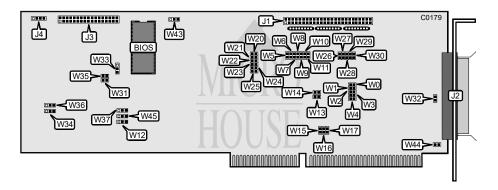
Q3/92

Data bus:	16-bit, ISA
Size:	Full-length, full-height card
Hard drives supported:	Up to seven SCSI devices
Floppy drives supported:	OMTI 81x: None
	OMTI 82x: Two 360KB, 720KB, 1.2MB, 1.44MB
Maximum heads/cyl.:	16 heads and 1024 cylinders

CONNECTIONS	
Function	Location
50-pin SCSI connector-internal	J1
50-pin SCSI connector-external (Models 8x2 only)	J2
34-pin control cable connector-floppy drive	J3
4-pin connector-drive active LED	J4

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í SCSI parity check enabled	W3	closed
SCSI parity check disabled	W3	open
í Factory configured - do not alter	W4	open
í Factory configured - do not alter	W10	open
í AT-type floppy drives supported (Models 82x only)	W11	open
Non-stand. floppy drives supported (Models 82x only)	W11	closed
Factory set on board 32K PROM size	W15	open
í Factory configured - do not alter	W26	open
í SCSI-2 RSVD lines grounded (terminators installed)	W32	closed
SCSI-2 RSVD lines not grounded (terminators removed)	W32	open
í Floppy drive port address is 3F0-3F7 (Models 82x only)	W33	pins 1 & 2 closed
Floppy drive port address is 370-377 (Models 820x only)	W33	pins 2 & 3 closed
í Floppy drive enabled	W35	closed
Floppy drive disabled	W35	open
í Power-ground and chassis-ground separate	W44	open
Power-ground and chassis-ground connected	W44	closed

SCSI ADDRESS			
Address	W0	W1	W2
0	closed	closed	closed
1	open	closed	closed
2	closed	open	closed
3	open	open	closed
4	closed	closed	open
5	open	closed	open
6	closed	open	open
í 7	open	open	open

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BIOS ADDRESS			
Select	W15	W16	W17
C0000h	closed	closed	closed
C8000h	open	closed	closed
D0000h	closed	open	closed
í D8000h	open	open	closed
E0000h	closed	closed	open
E8000	open	closed	open
F0000h	closed	open	open
F8000h	open	open	open

I/O PORT ADDRESS					
Address	W5	W6	W7	W8	W9
100	closed	closed	closed	open	closed
120	open	closed	closed	open	closed
140	closed	open	closed	open	closed
160	open	open	closed	open	closed
180	closed	closed	open	open	closed
1A0	open	closed	open	open	closed
1C0	closed	open	open	open	closed
1E0	open	open	open	open	closed
200	closed	closed	closed	closed	open
220	open	closed	closed	closed	open
240	closed	open	closed	closed	open
260	open	open	closed	closed	open
280	closed	closed	open	closed	open
2A0	open	closed	open	closed	open
2C0	closed	open	open	closed	open
2E0	open	open	open	closed	open
300	closed	closed	closed	open	open
320	open	closed	closed	open	open
í 340	closed	open	closed	open	open
360	open	open	closed	open	open
380	closed	closed	open	open	open
3A0	open	closed	open	open	open
3C0	closed	open	open	open	open
3E0	open	open	open	open	open

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DMA CHANNEL						
Channel	W20	W21	W22	W23	W24	W25
DRQ3	closed	open	open	open	closed	closed
í DRQ5	open	closed	open	open	closed	open
DRQ6	open	open	closed	open	open	closed
DRQ7	open	open	open	closed	open	open

INTERRUPT REQUEST				
IRQ	W27	W28	W29	W30
Disabled	N/A	N/A	N/A	closed
IRQ3	closed	closed	closed	open
IRQ4	open	closed	closed	open
IRQ5	closed	open	closed	open
IRQ6	open	closed	closed	open
IRQ7	closed	closed	open	open
í IRQ12	open	closed	open	open
IRQ14	closed	open	open	open
IRQ15	open	open	open	open

MISCELLANEOUS TECHNICAL NOTES
W12-14, W31, W34, W36, W37, W43, W45 are factory configurable or Not used and are hard wired on some board revisions. The default for these jumpers is "open", with a jumper stored on pin 2.